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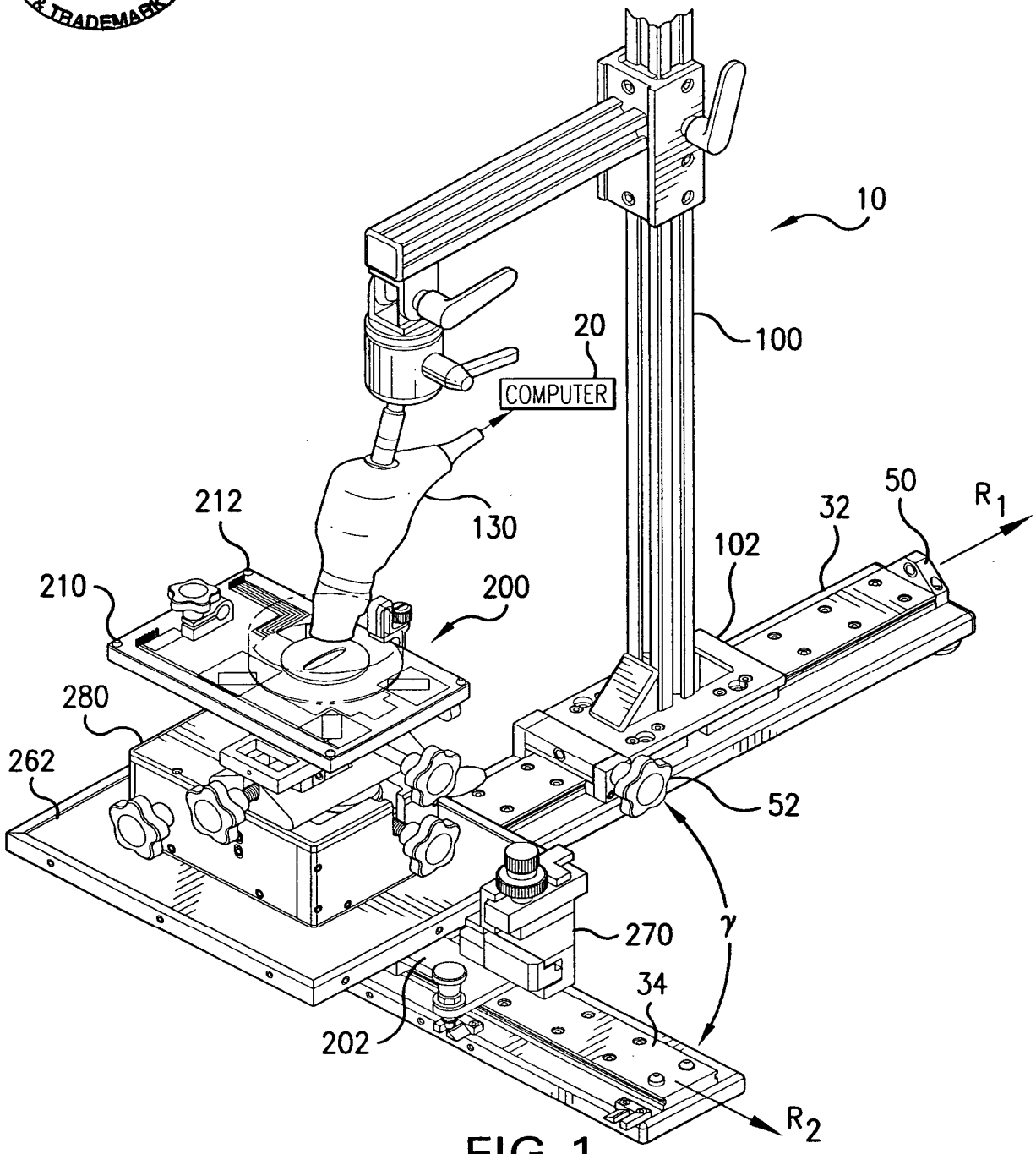


FIG. 1

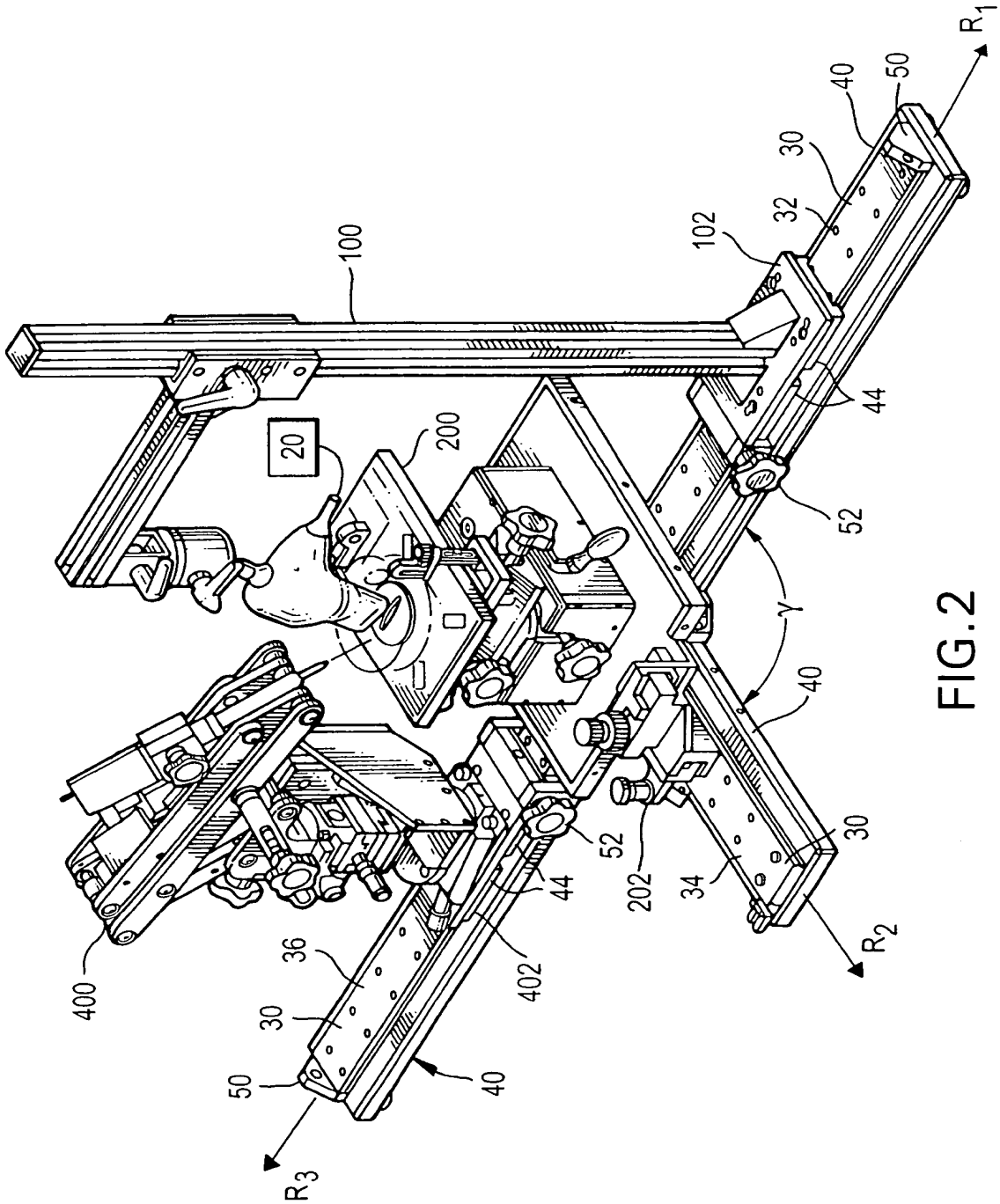

R1
40
50
30
32
102
100
44
20
200
52
γ
40
30
52
202
34
44
R2
36
402
400
30
50
40
R3

FIG.2

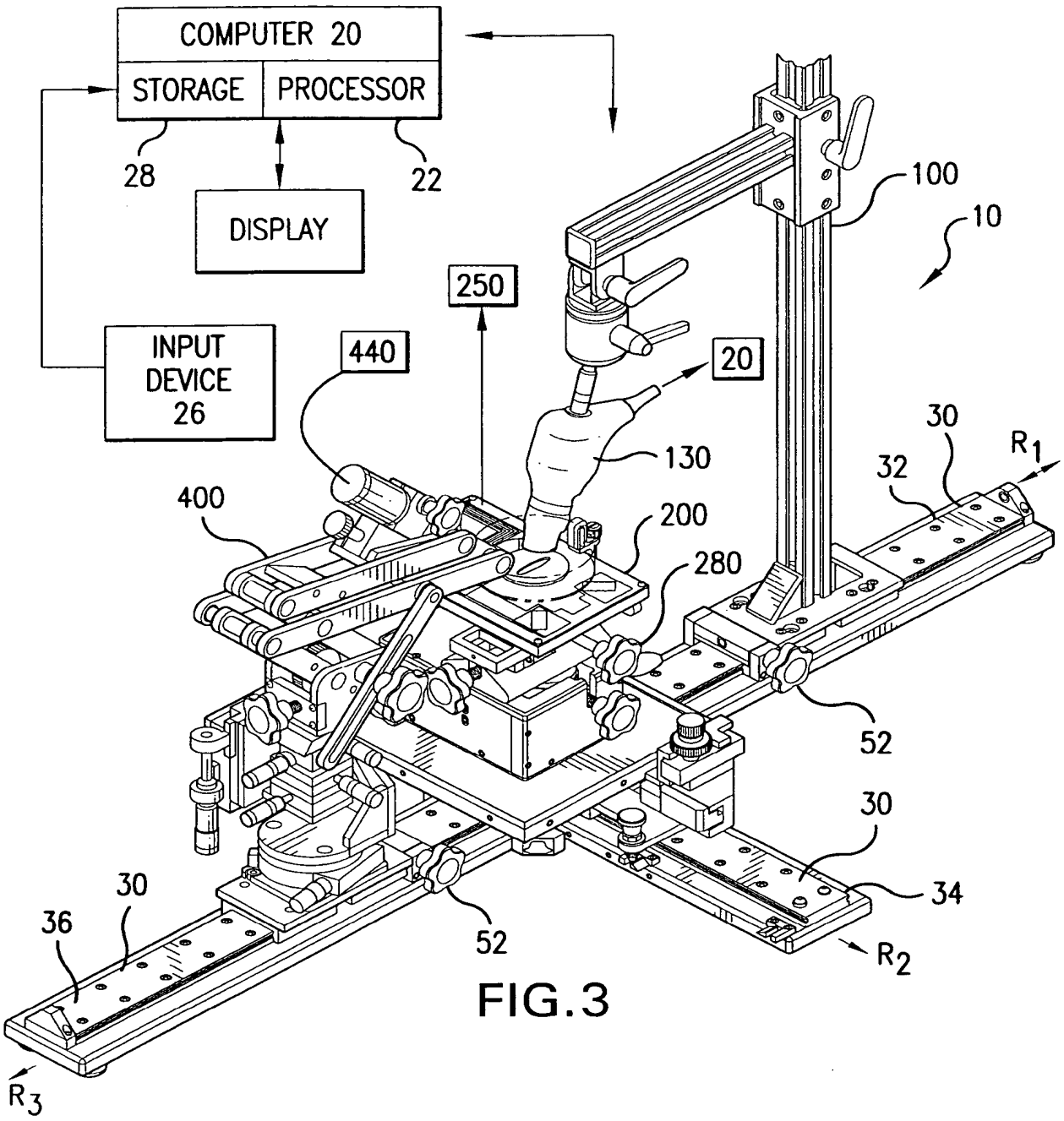

COMPUTER 20
STORAGE
PROCESSOR
28
22
DISPLAY
INPUT DEVICE 26
250
440
20
100
10
130
400
200
280
30
32
R_1
52
30
34
R_2
30
36
52
R_3

FIG.3

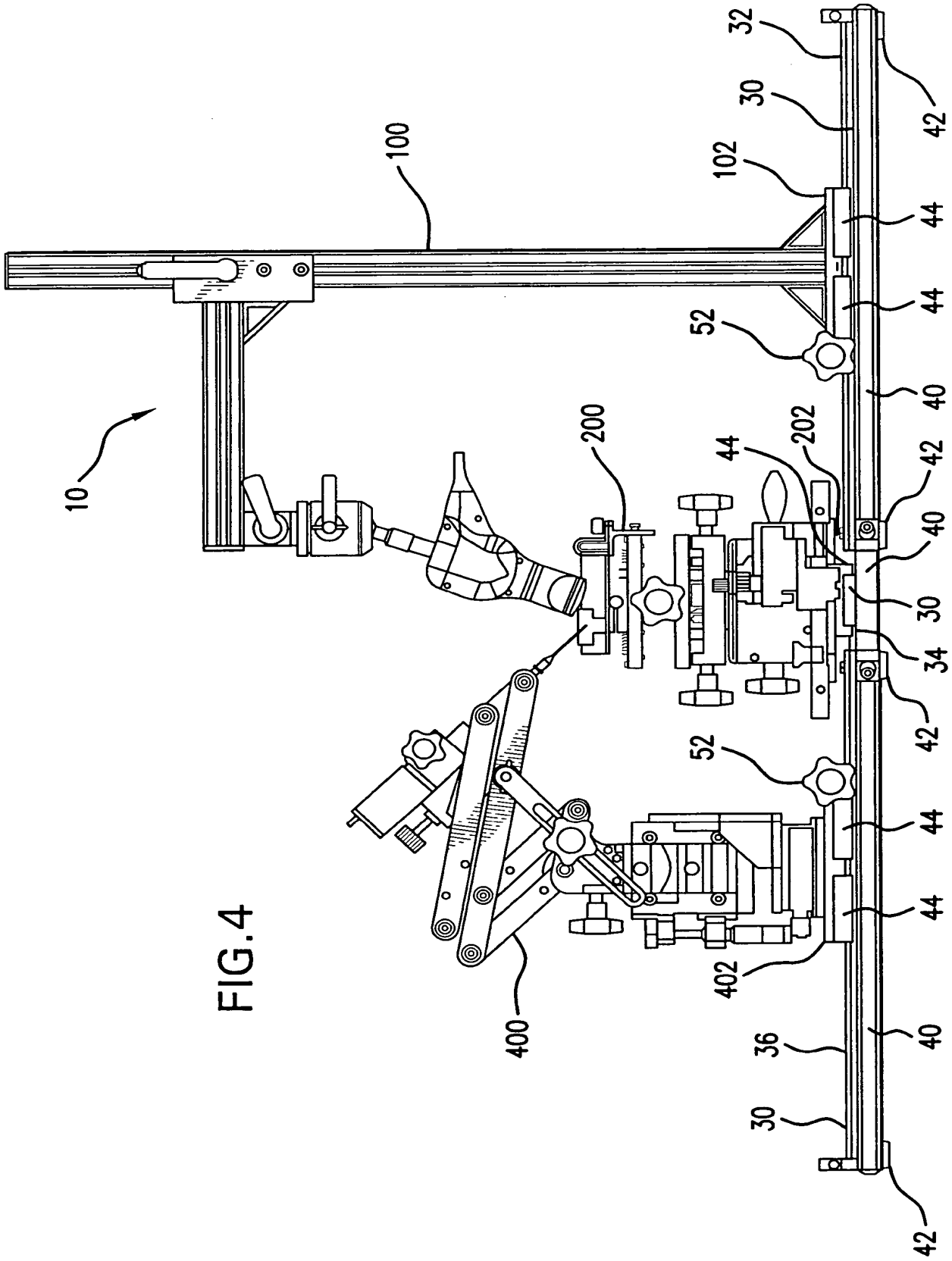


FIG.4

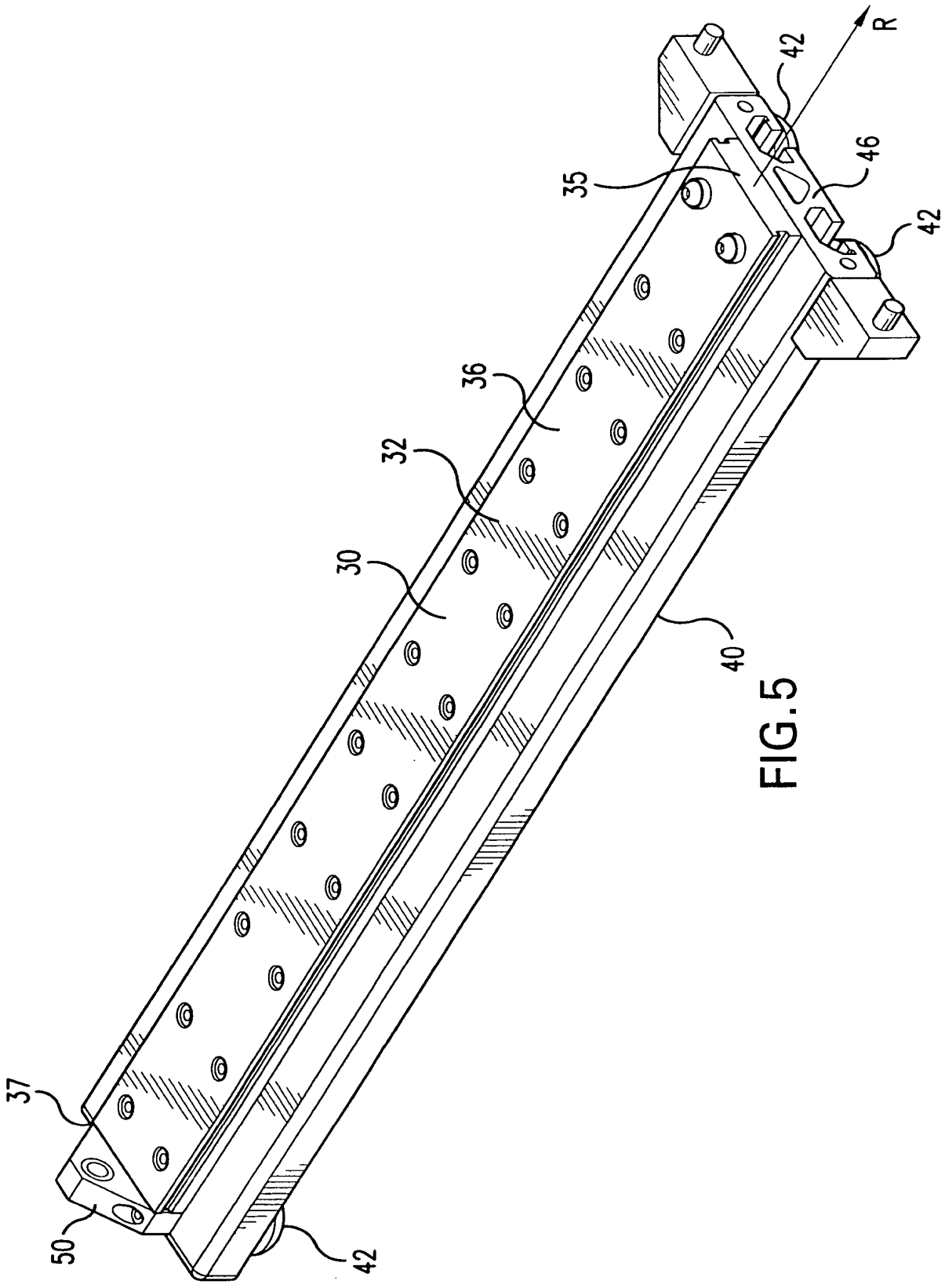


FIG.5

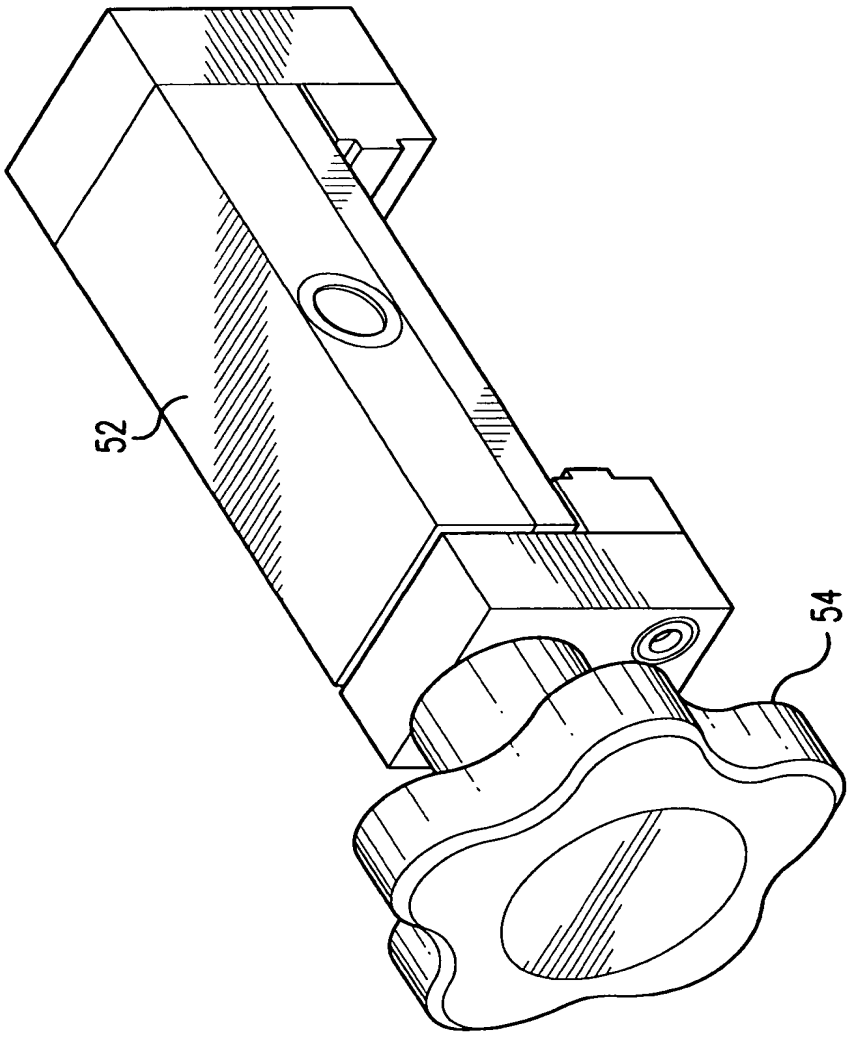


FIG.6

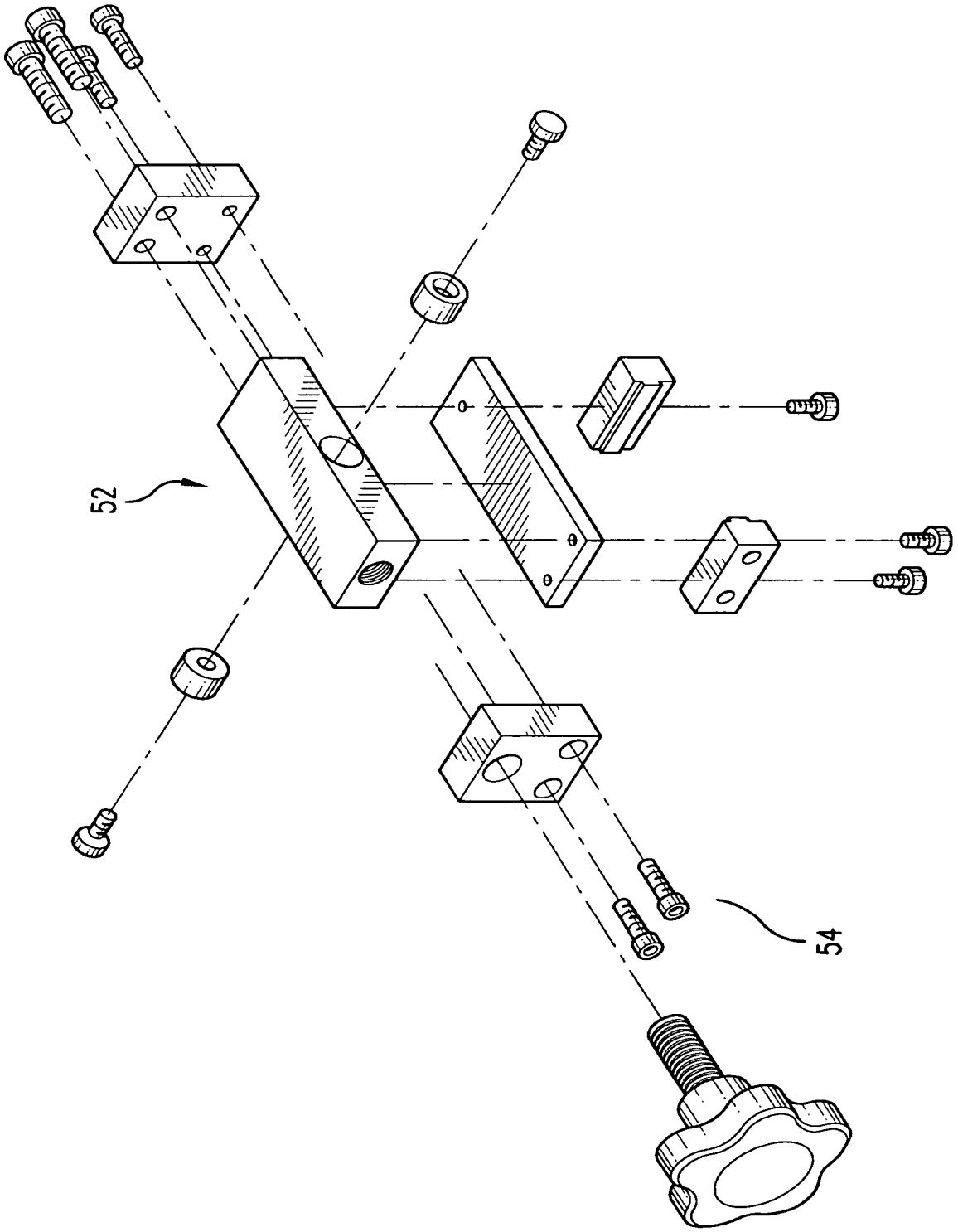


FIG.7

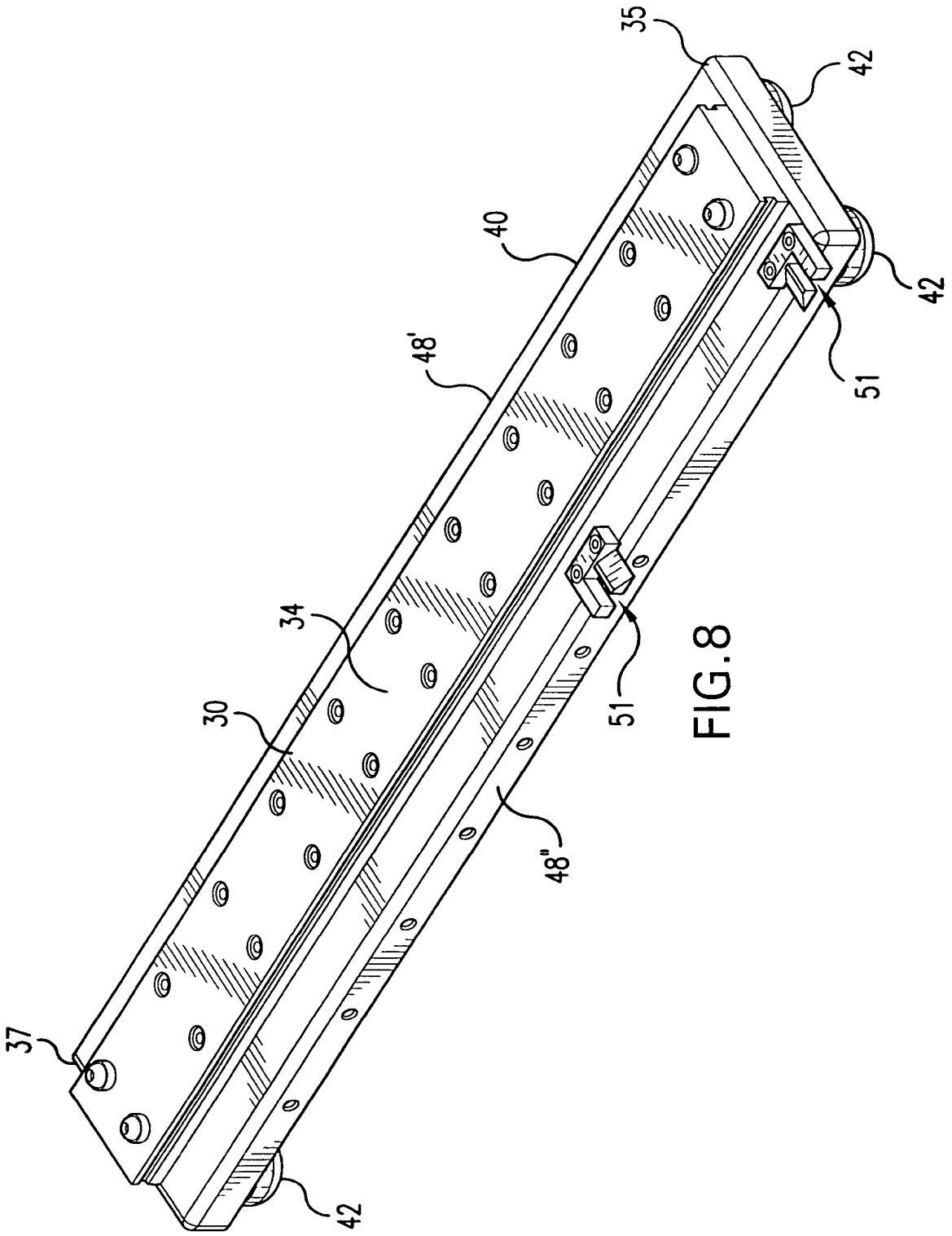
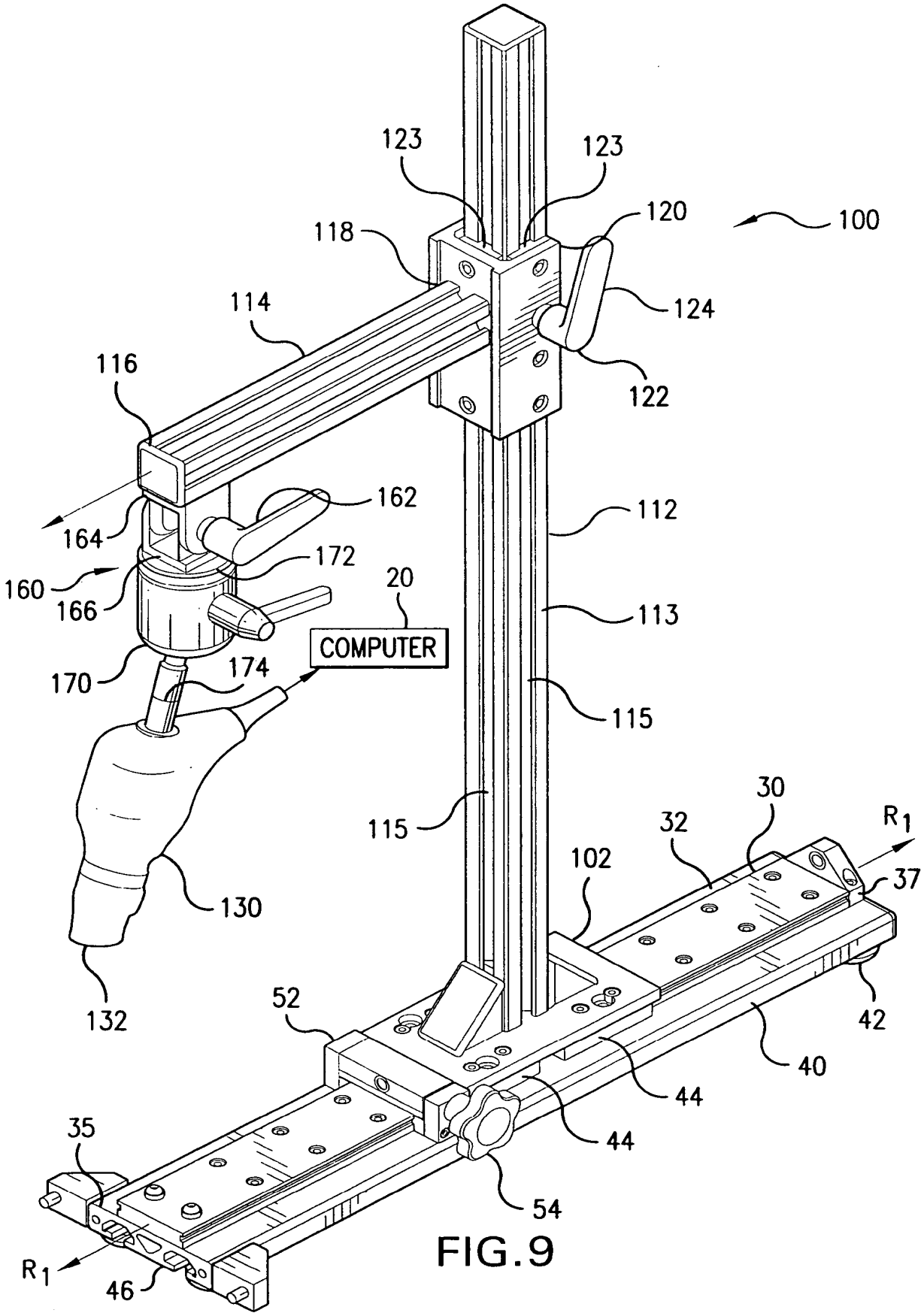


FIG.8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100
118
124
114
116
122
162
112
164
172
160
166
20
113
COMPUTER
170
174
115
115
30
32
R_1
102
37
130
52
42
132
40
44
44
35
54
R_1
46

FIG.9

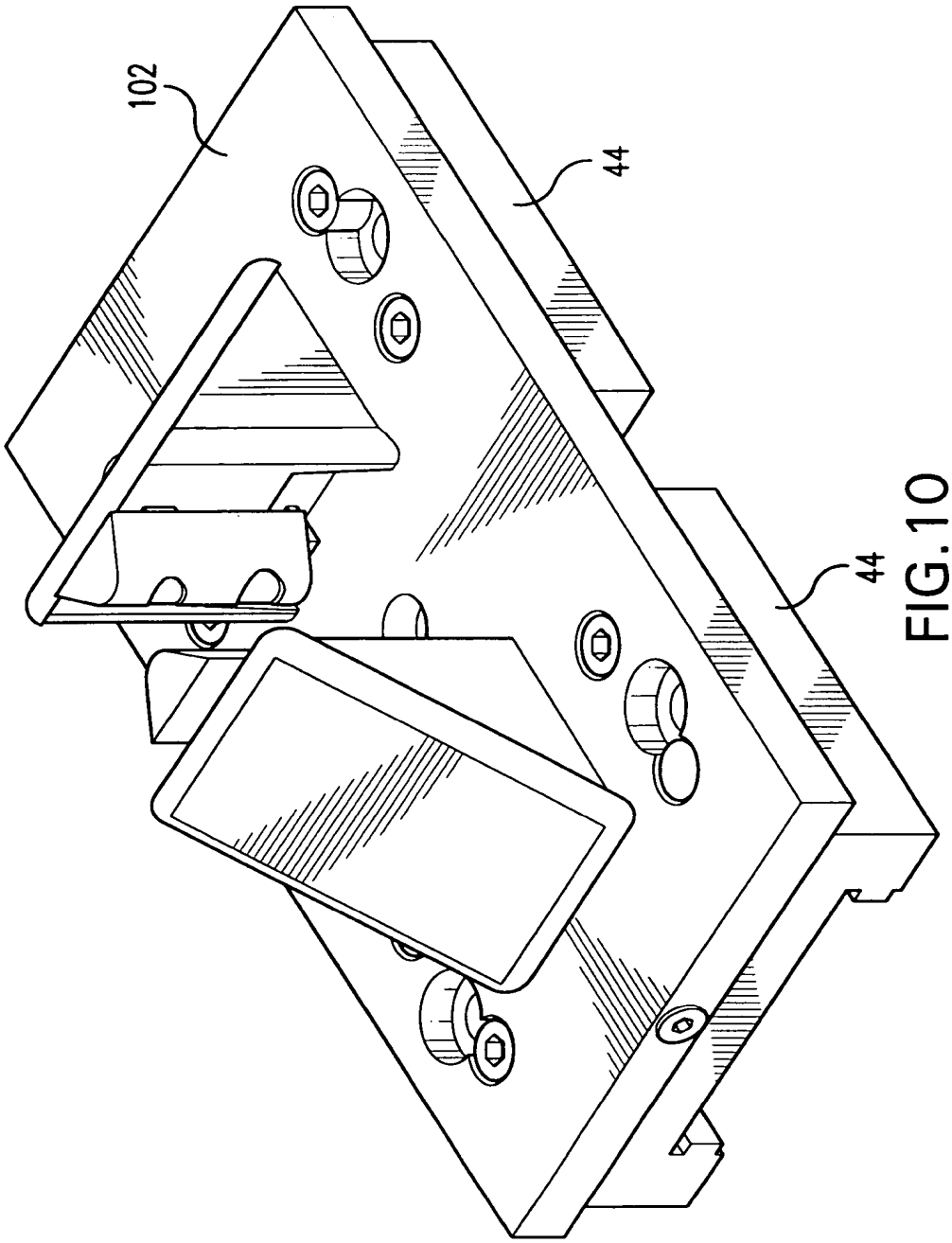
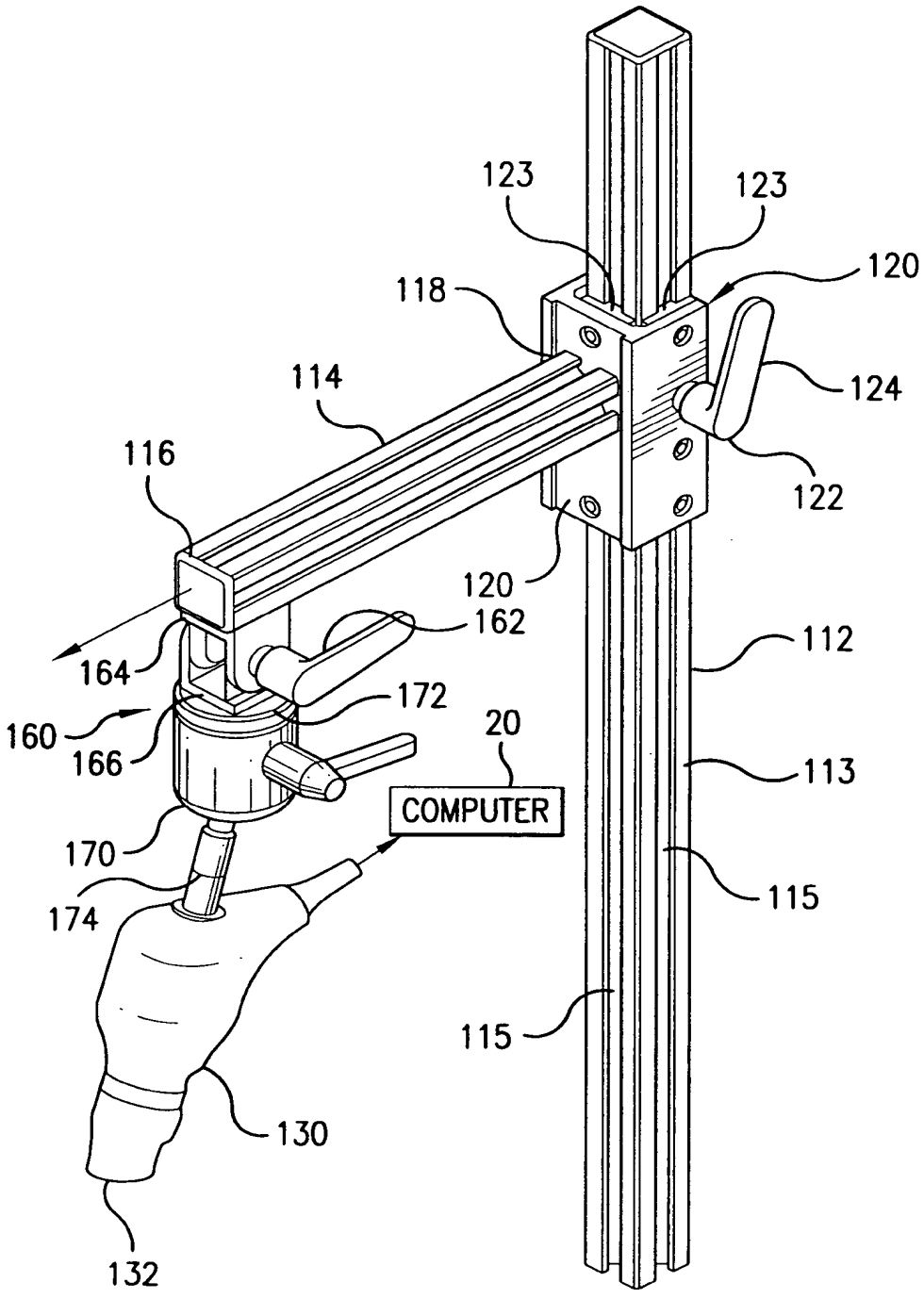


FIG.10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COMPUTER
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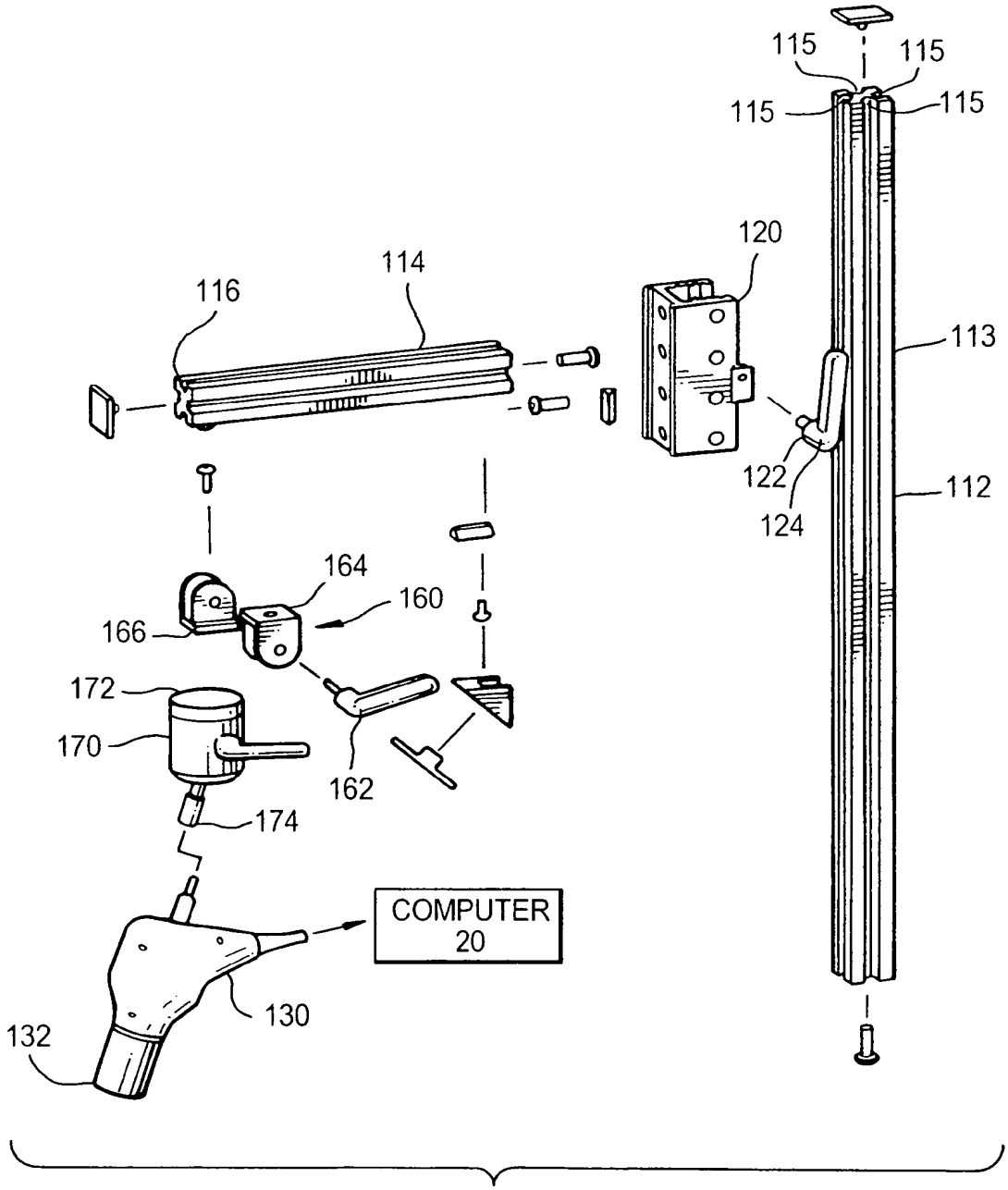
115
174
115
130
132

FIG11


115
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124
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160
166
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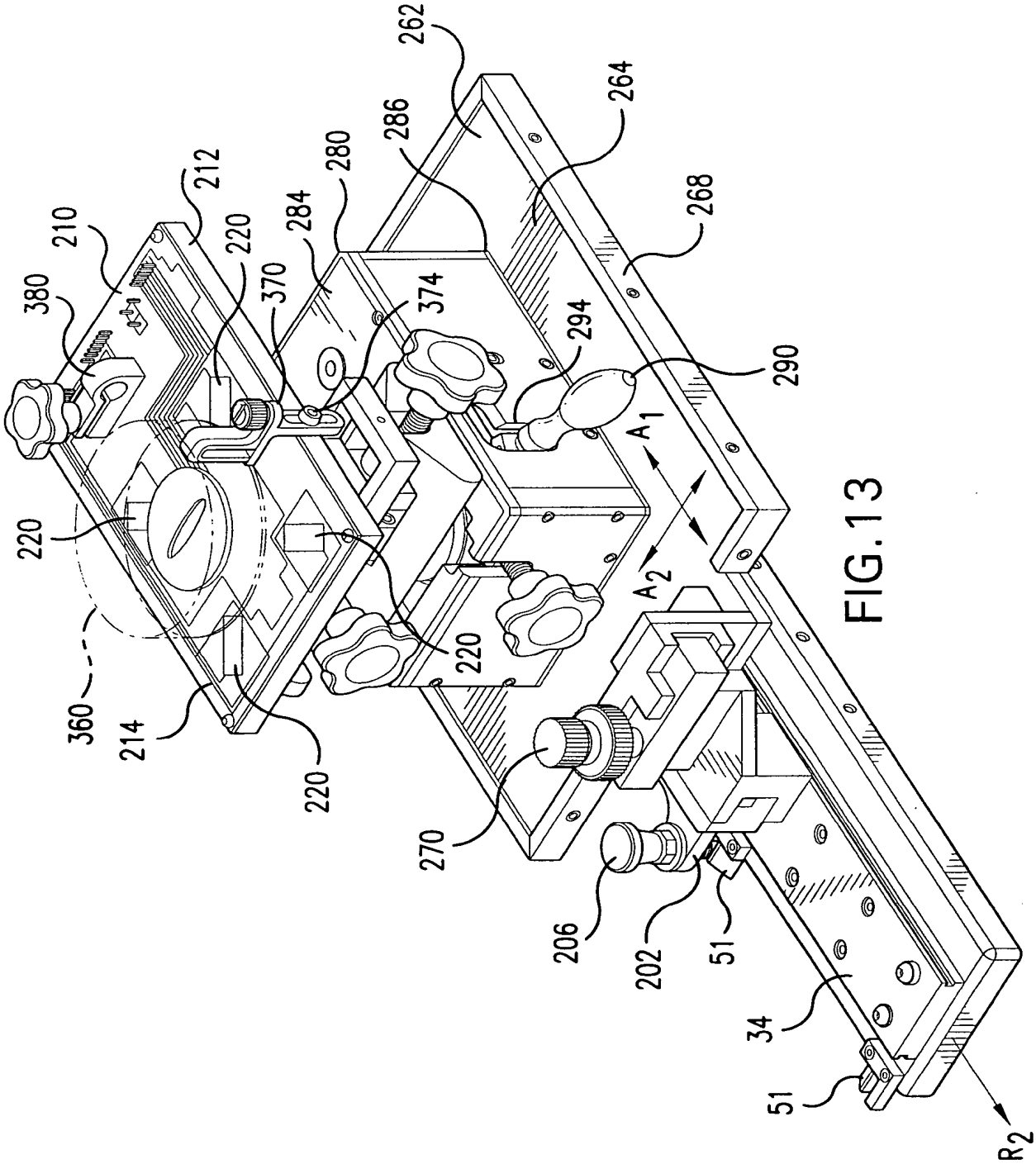
162
174
COMPUTER
20
130
132

FIG. 12


262
264
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212
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268
380
370
374
294
290
220
A_1
A_2
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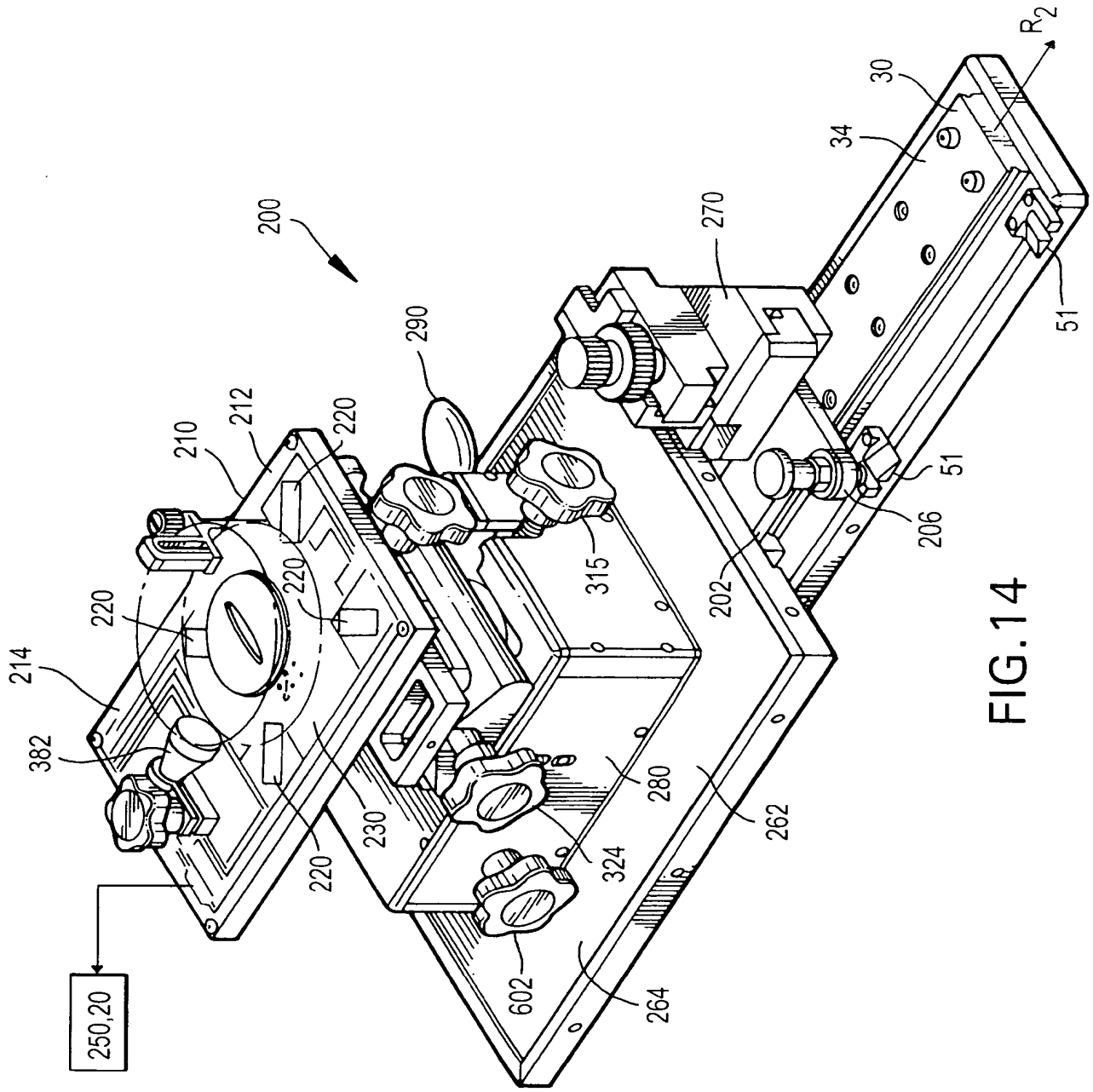
360
214
220
270
206
202
51
34
51
R_2

FIG. 13


200
290
270
30
34
R_2
51
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212
220
315
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202
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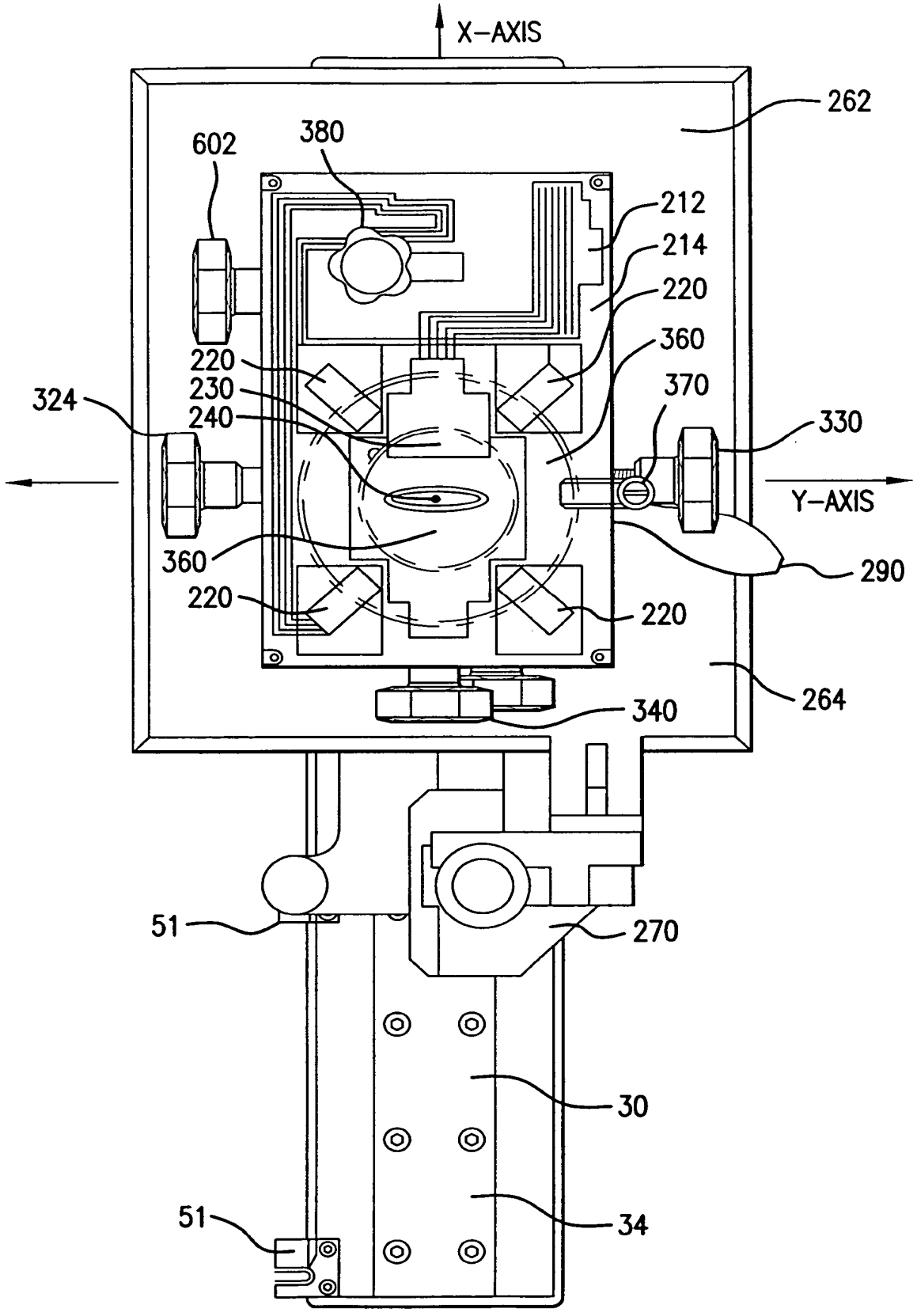
382
230
280
262
324
602
264
250,20

FIG. 14


X-AXIS
262
602
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214
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360
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324
330
Y-AXIS
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360
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220
264
340
51
270
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51
34

FIG.15

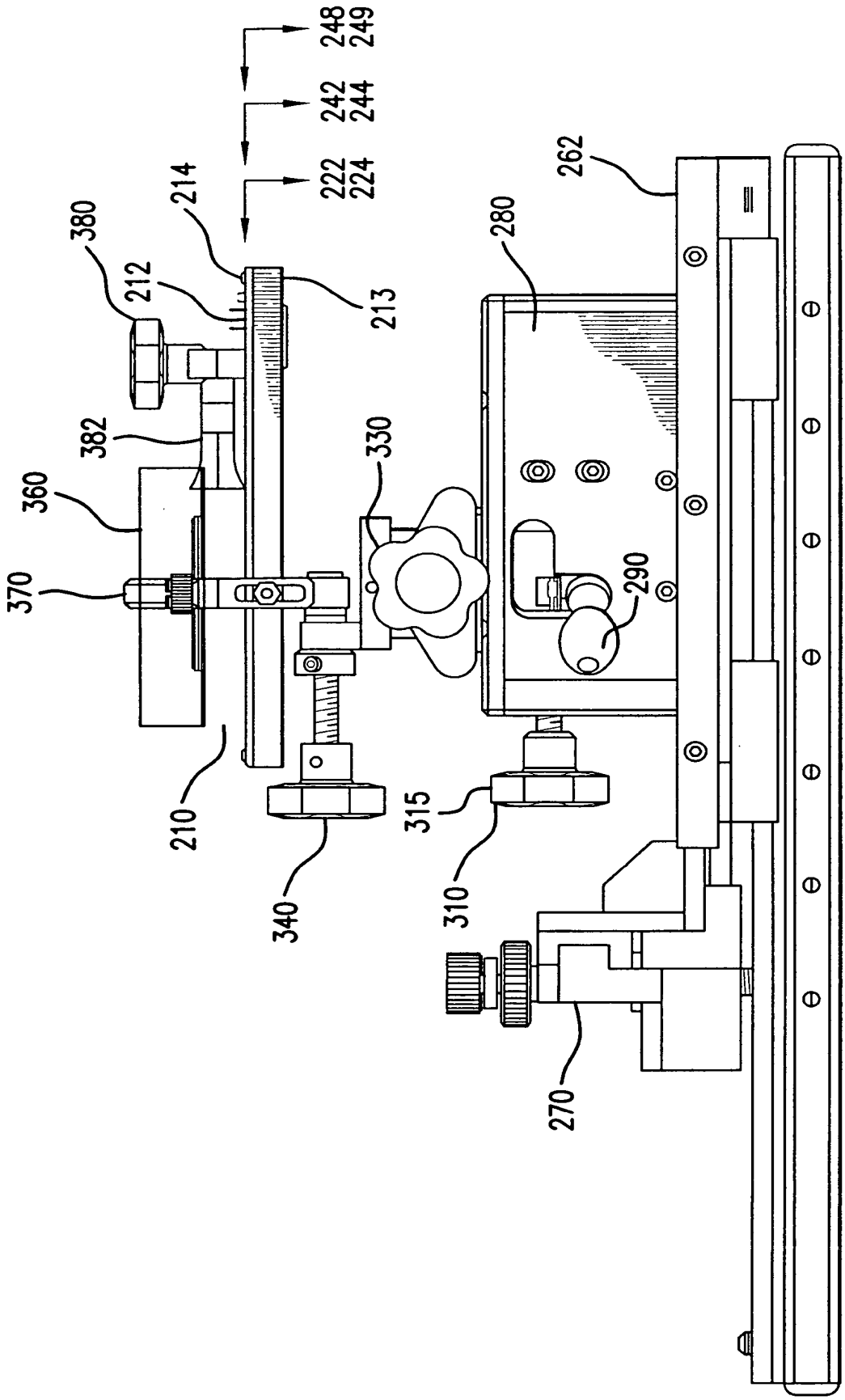
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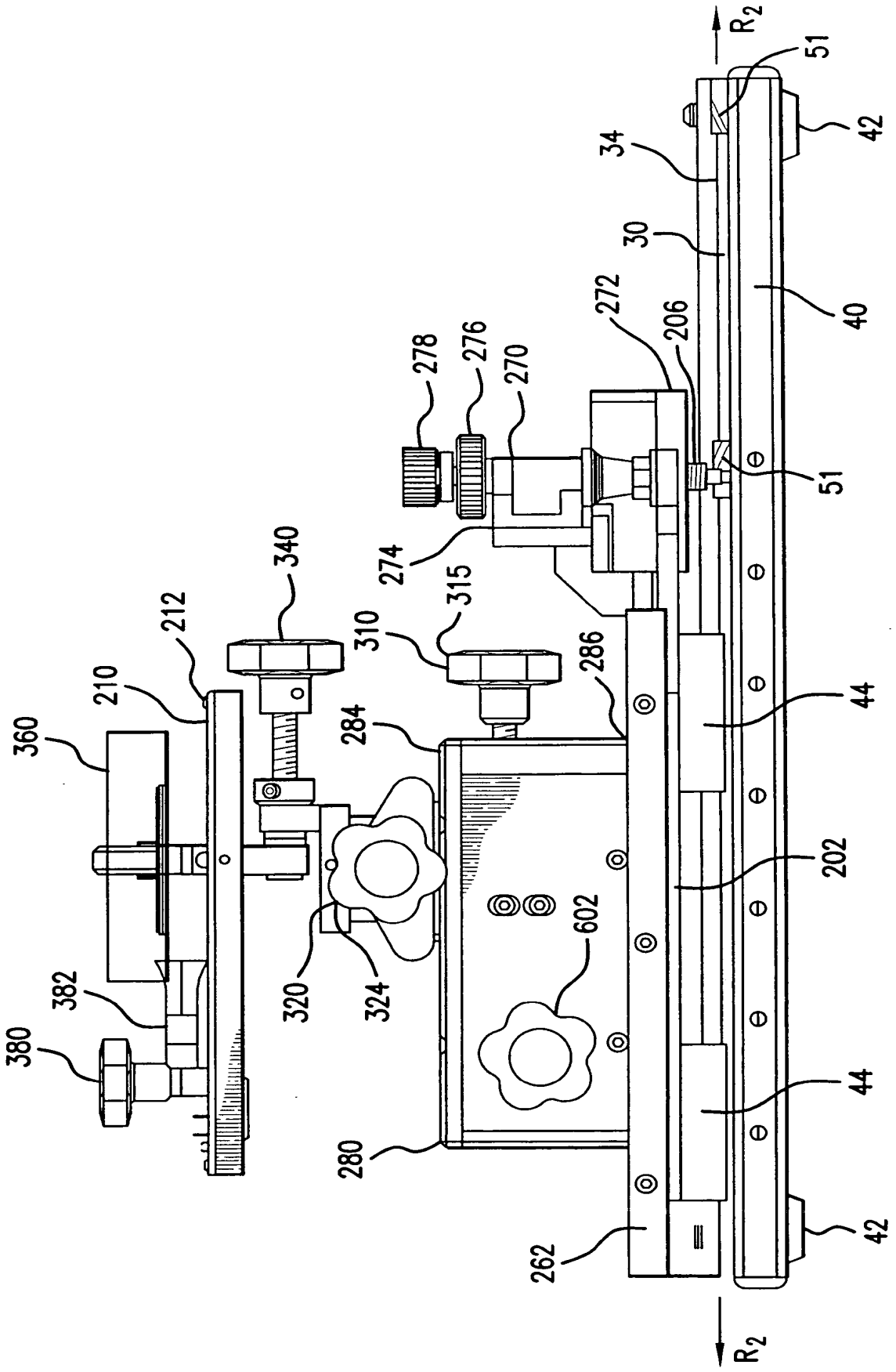
FIG.16


R2
51
42
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30
40
272
206
278
276
270
51
274
340
315
212
310
286
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360
284
44
202
602
382
320
324
380
280
44
262
42
R2

FIG. 17

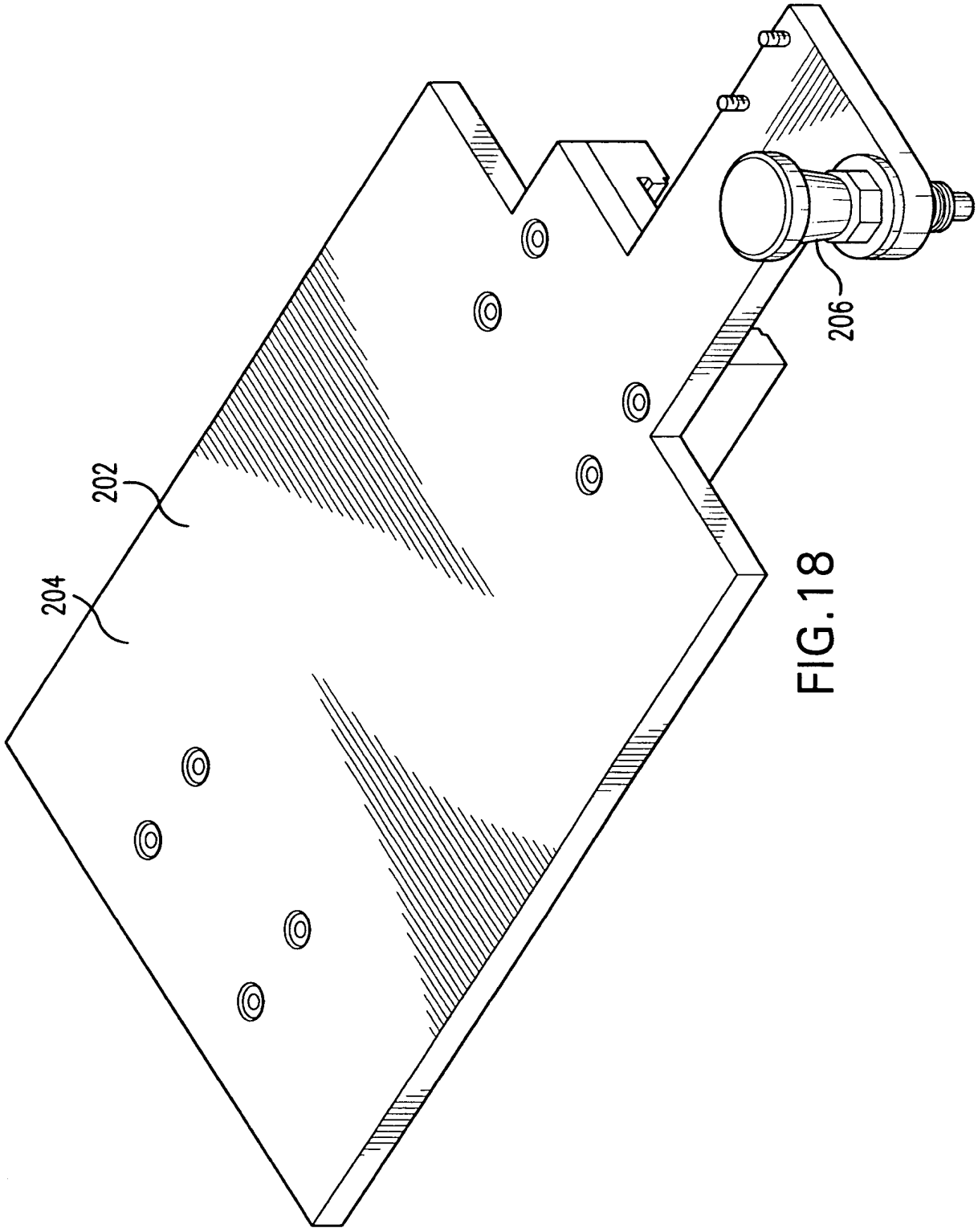

FIG. 18

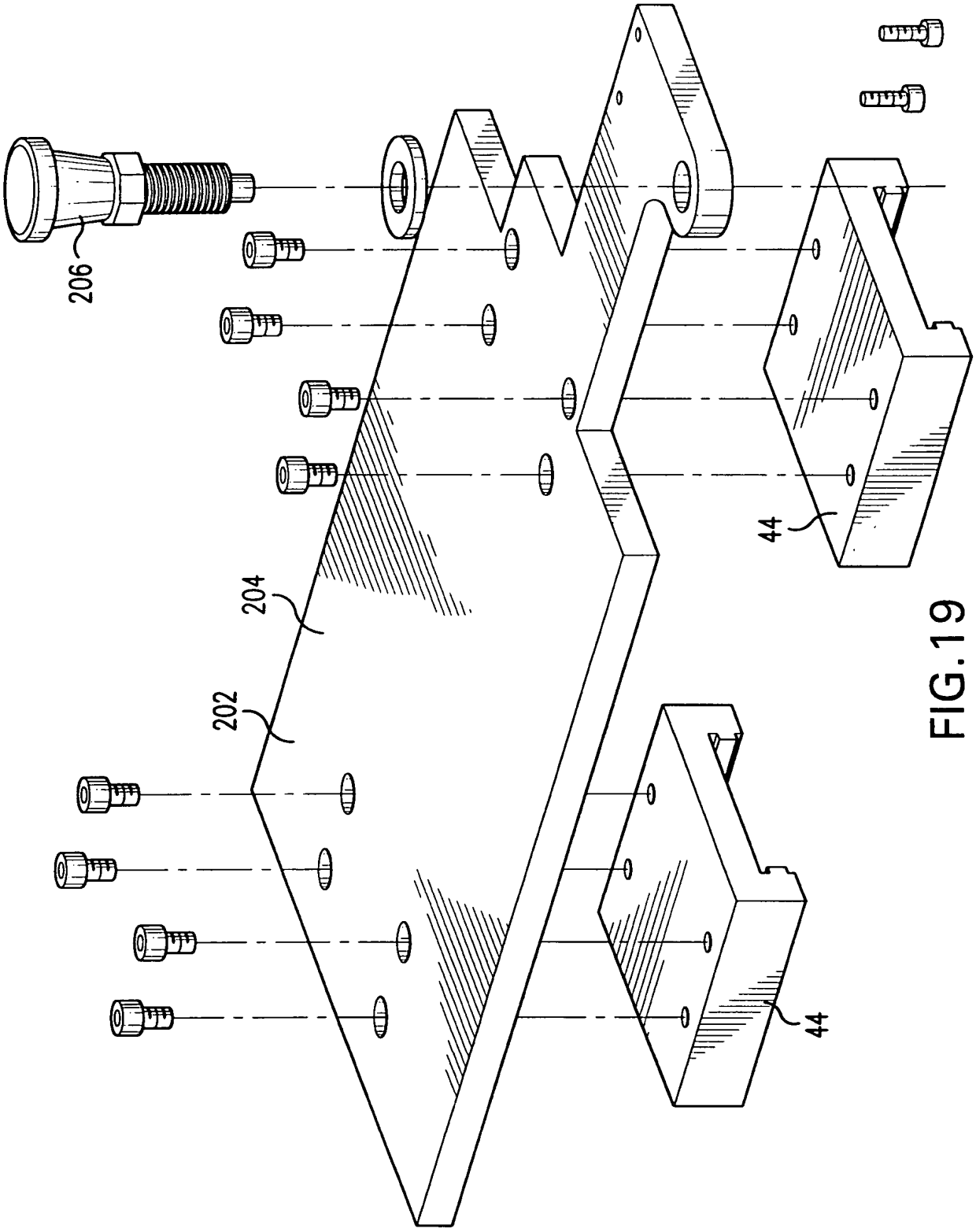

FIG. 19

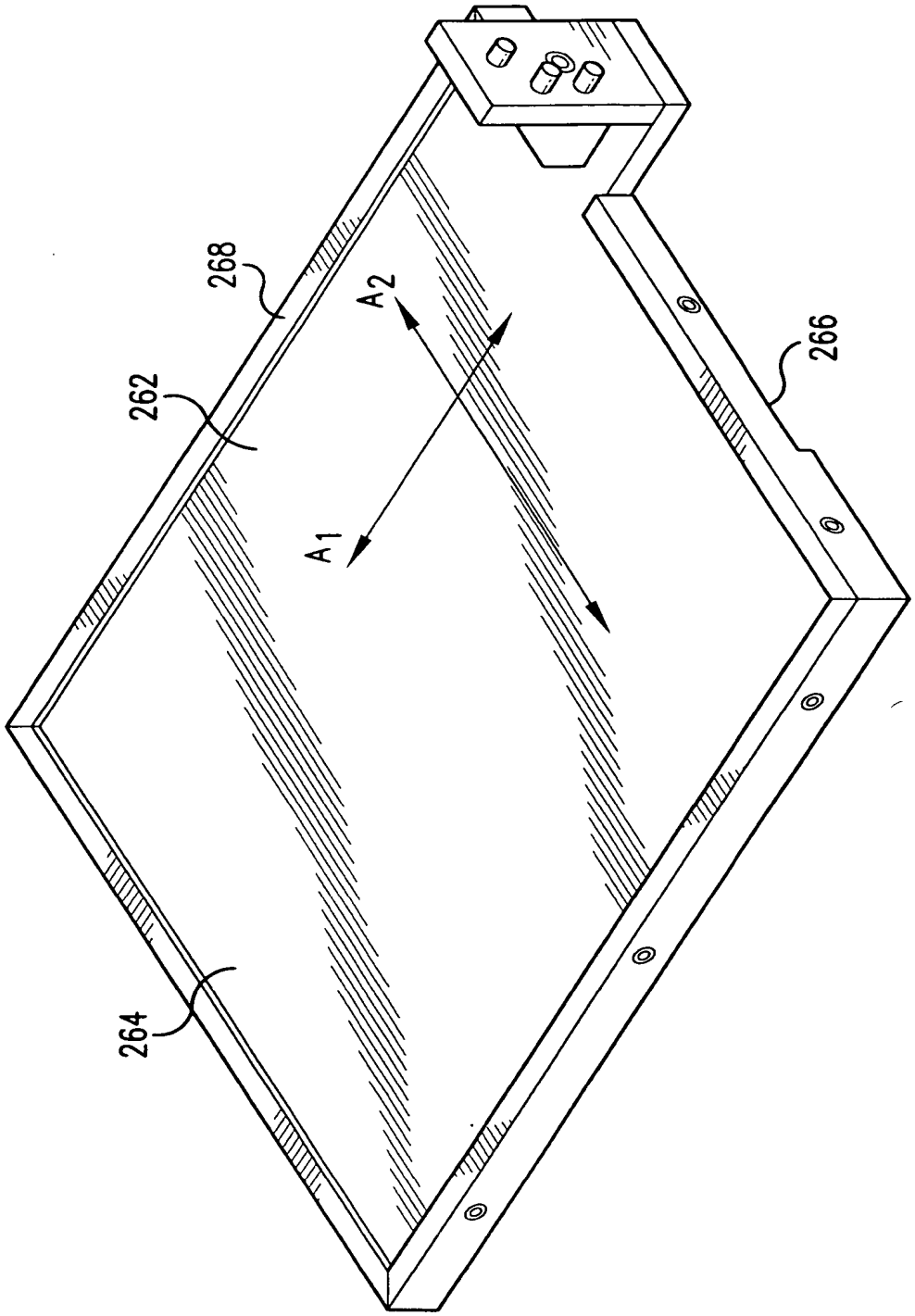

FIG.20

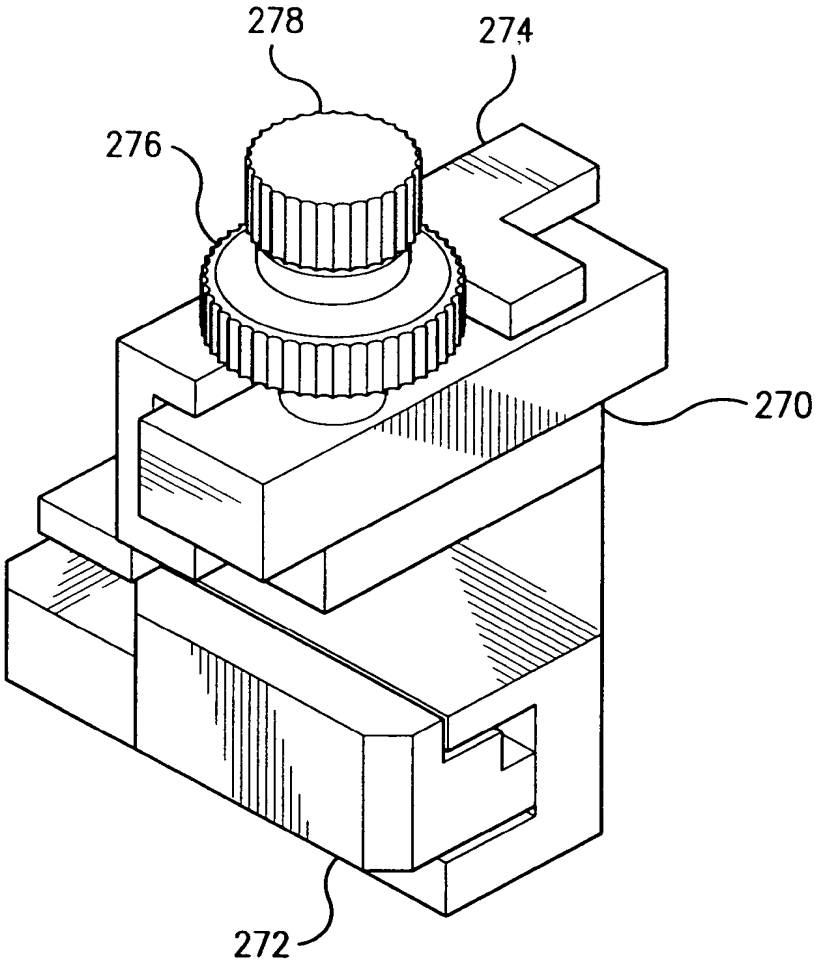

FIG.21

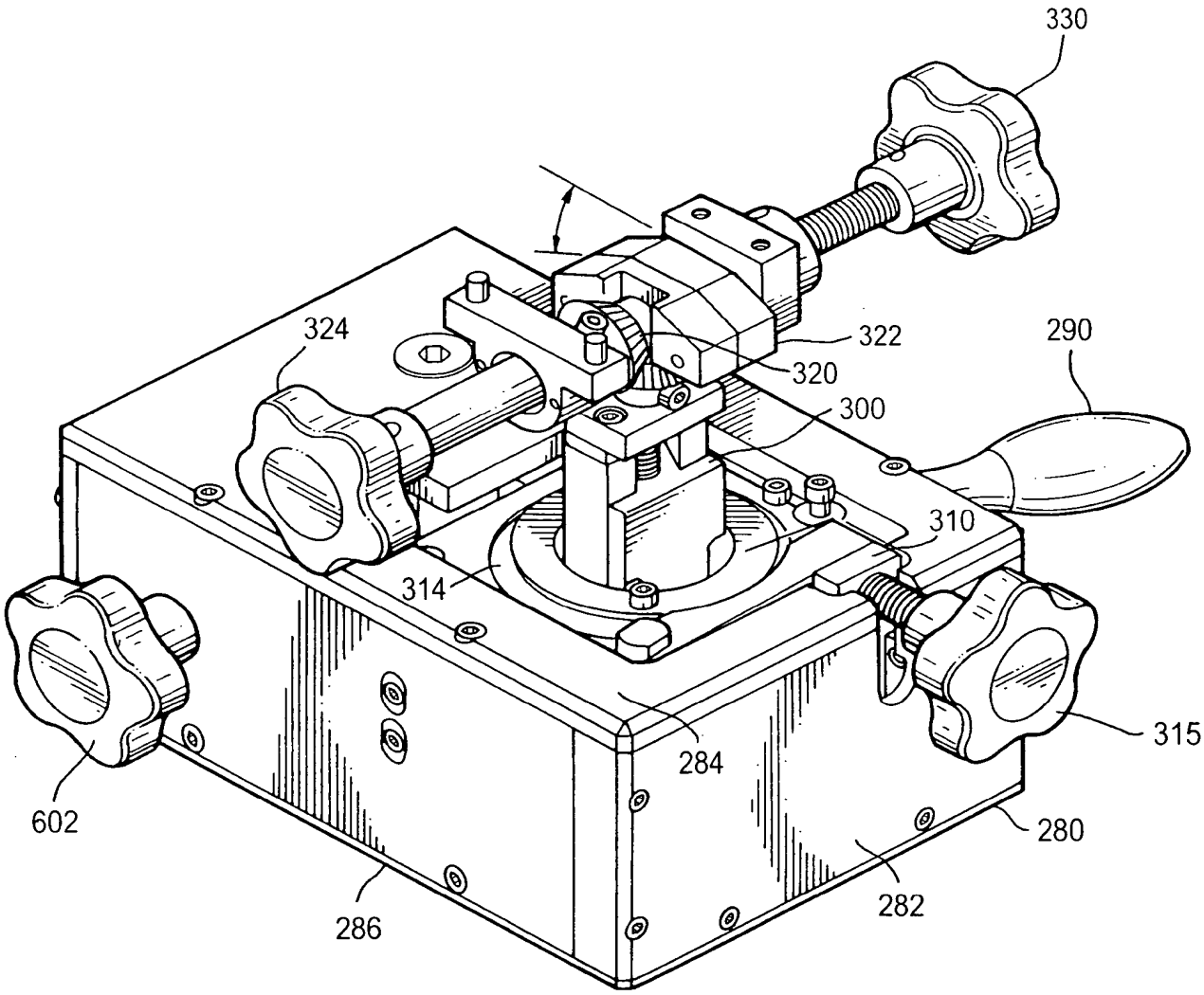

FIG.22

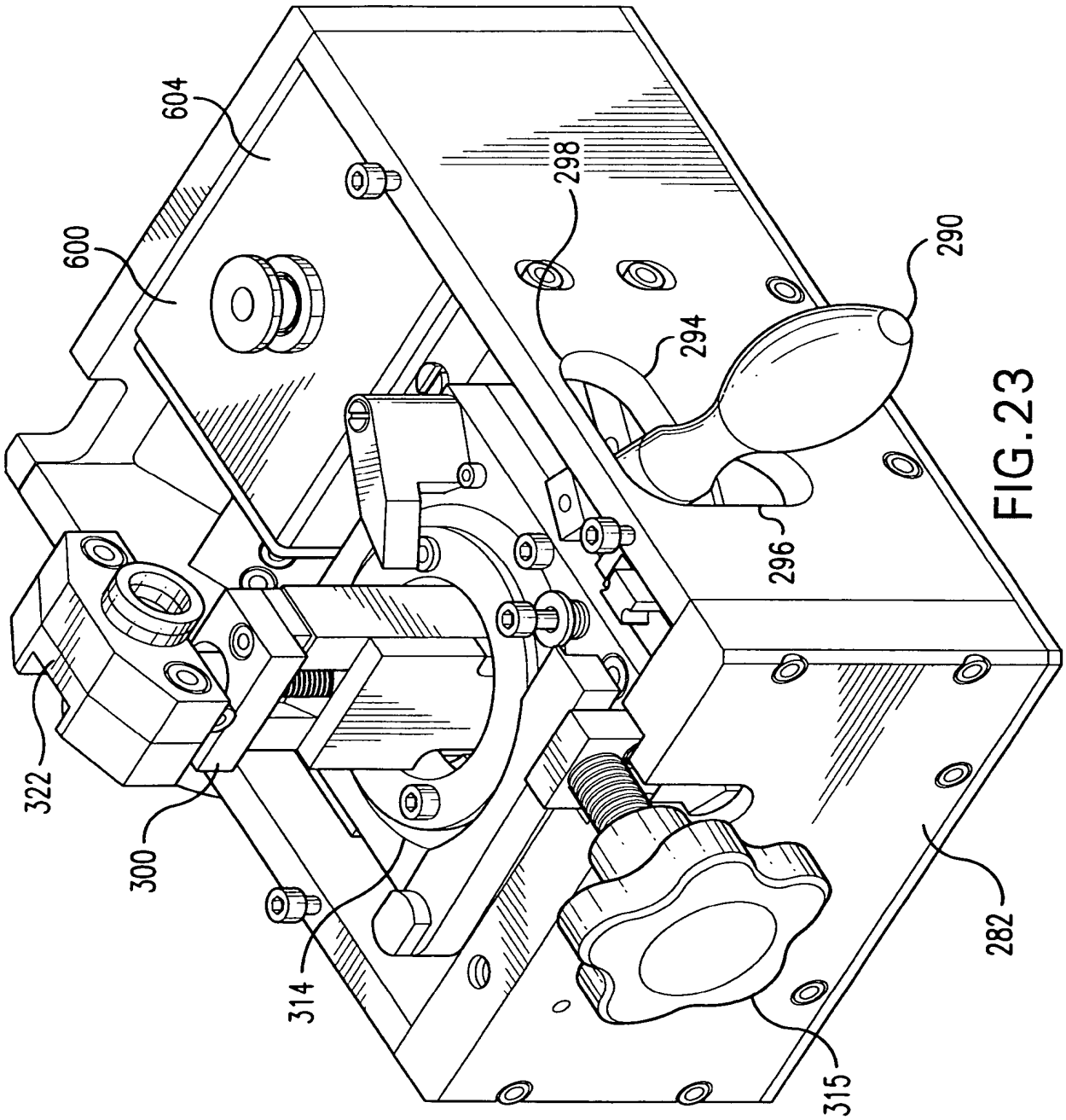

FIG.23

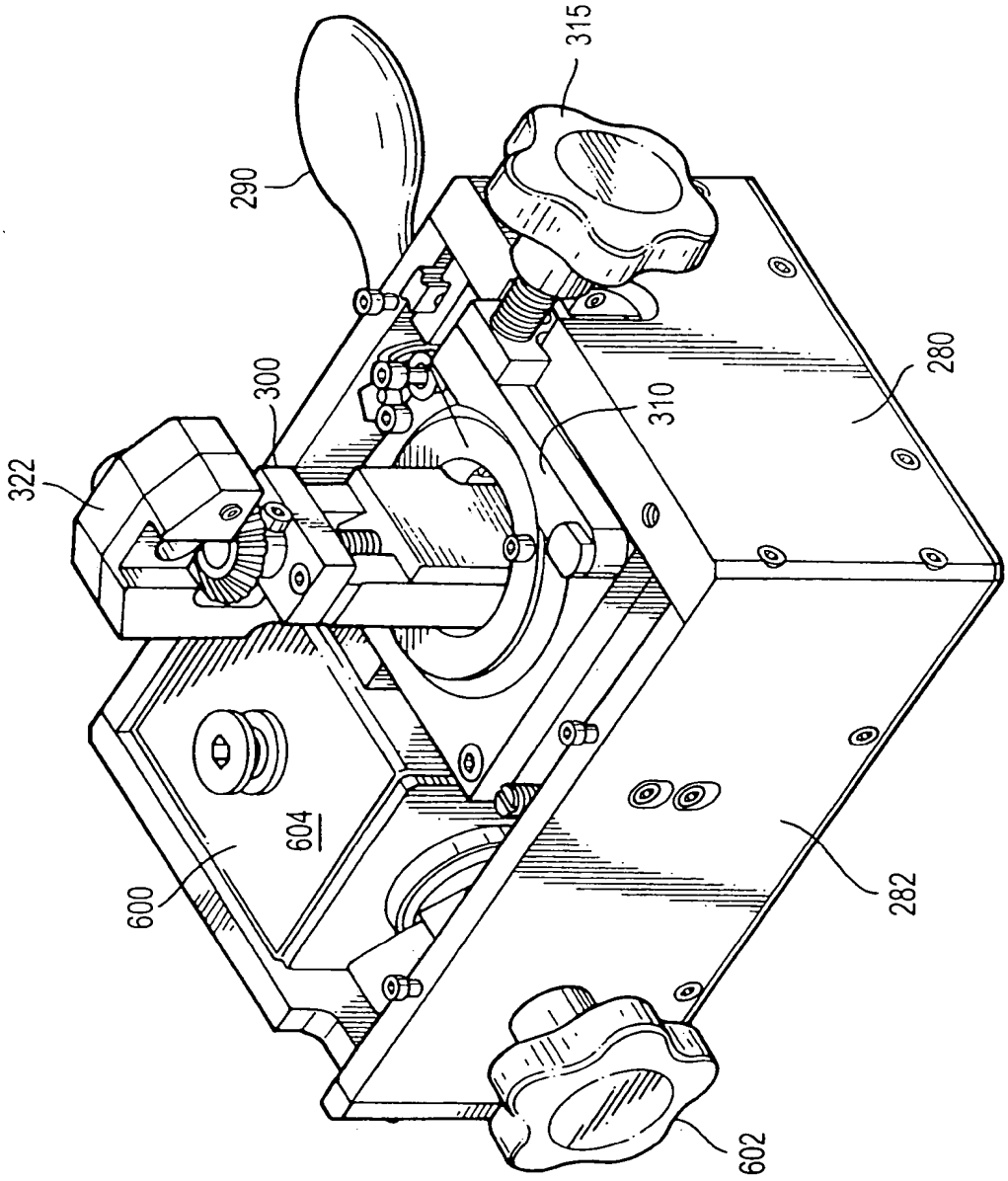

FIG.24

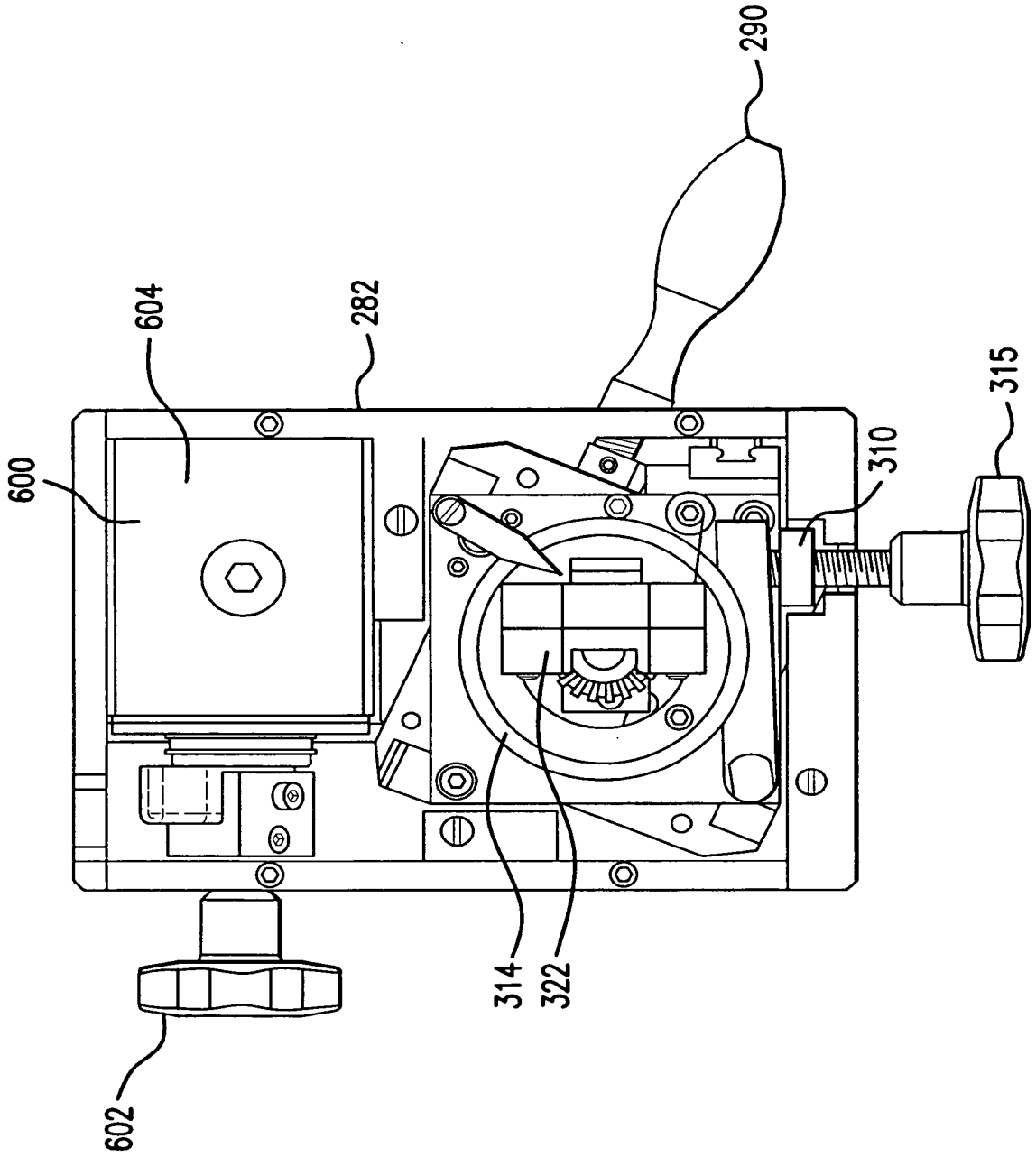

FIG.25

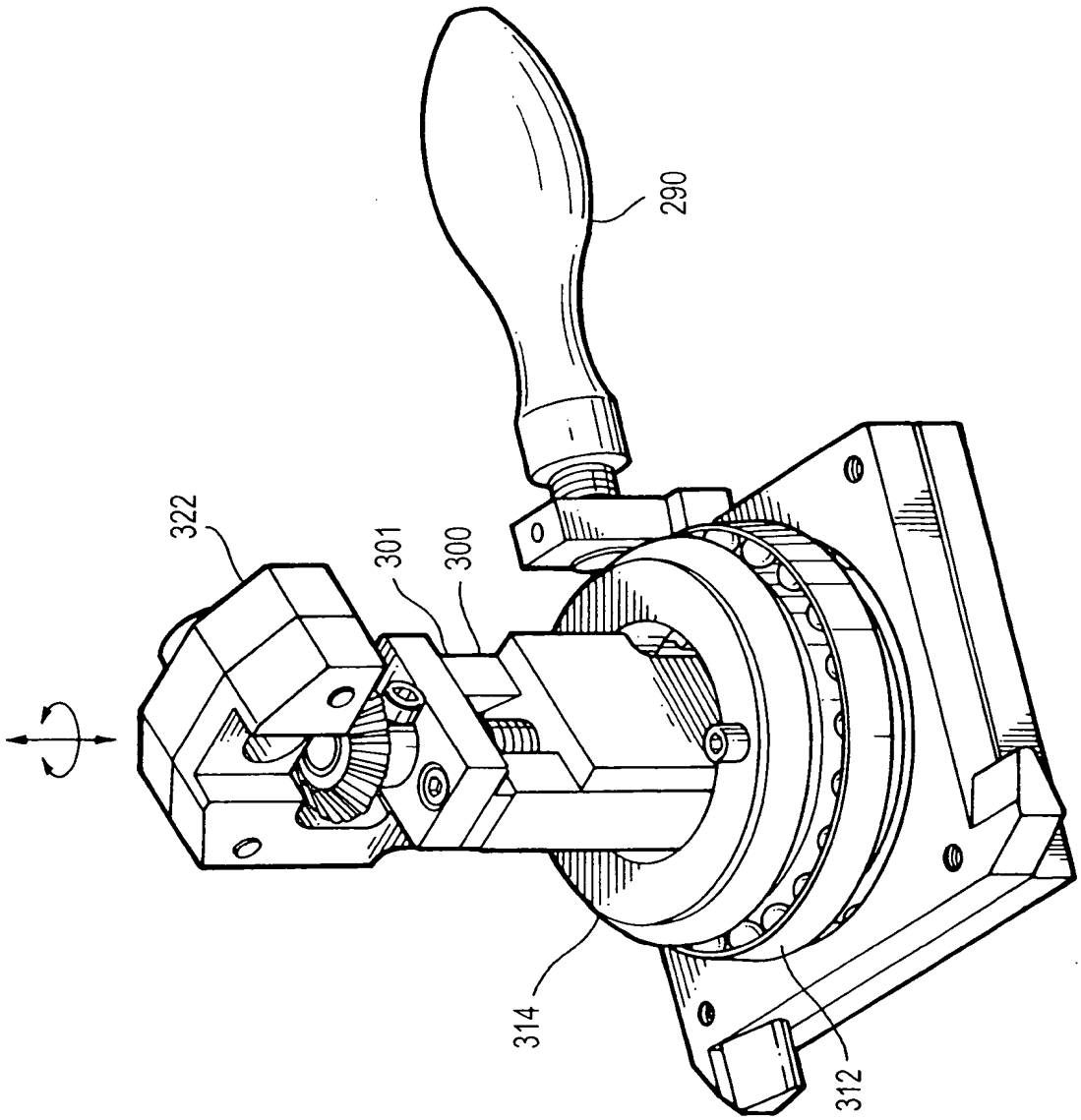

FIG.26

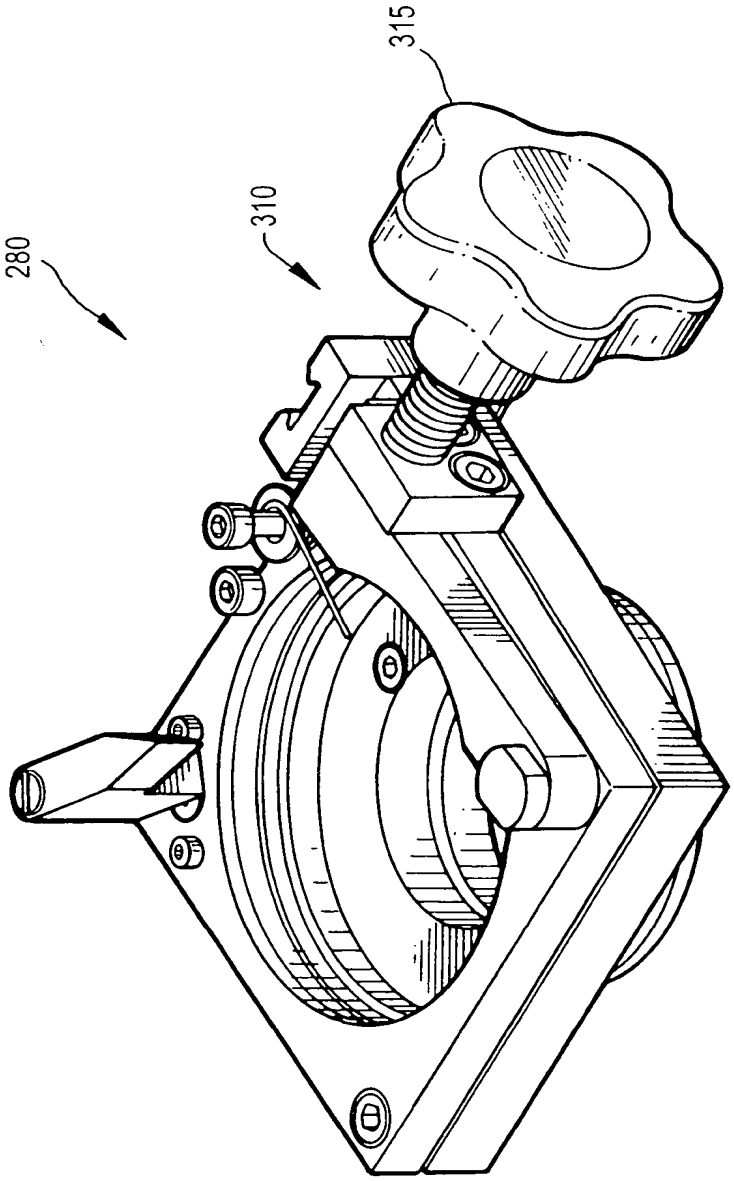

FIG.27

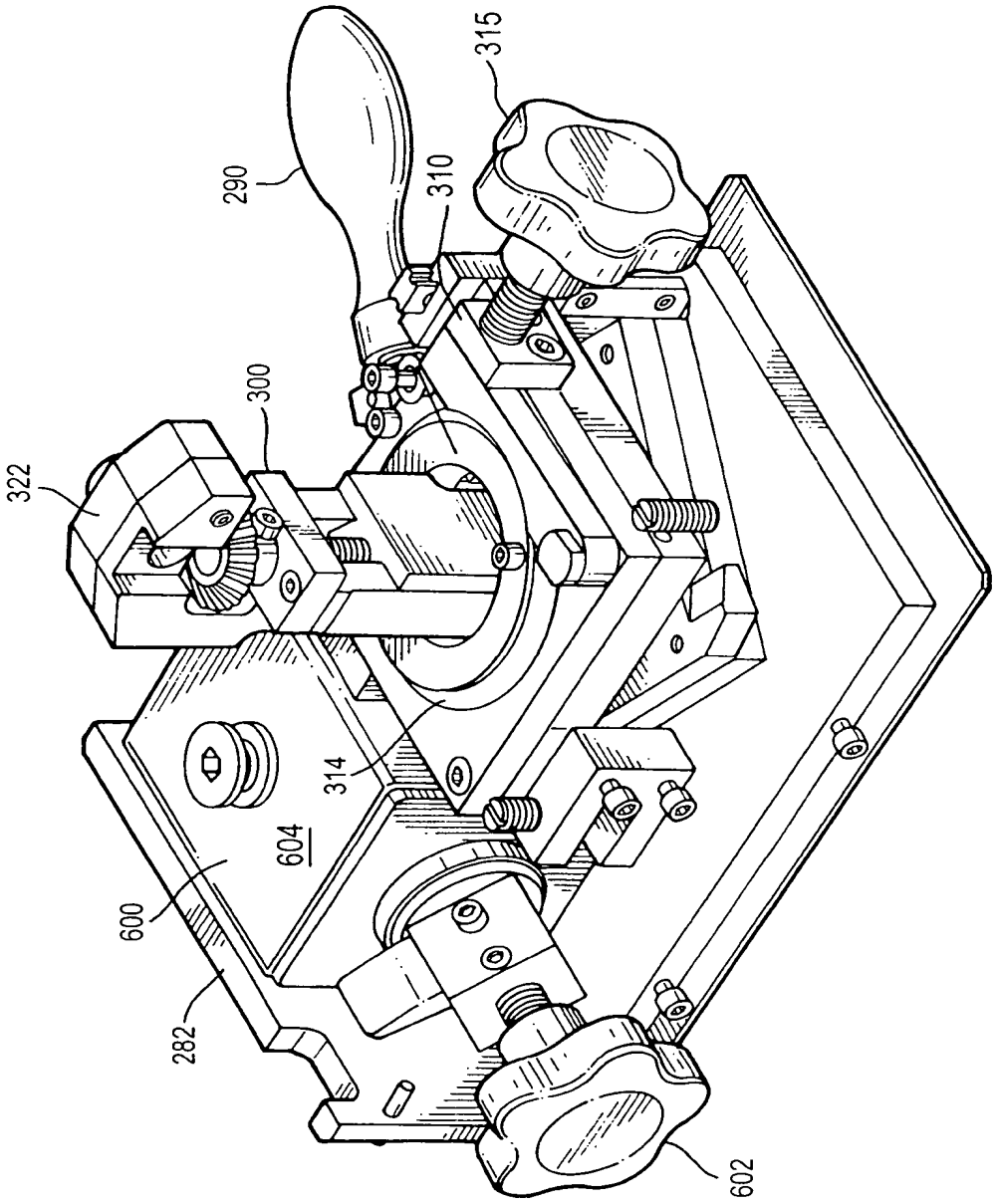

FIG.28

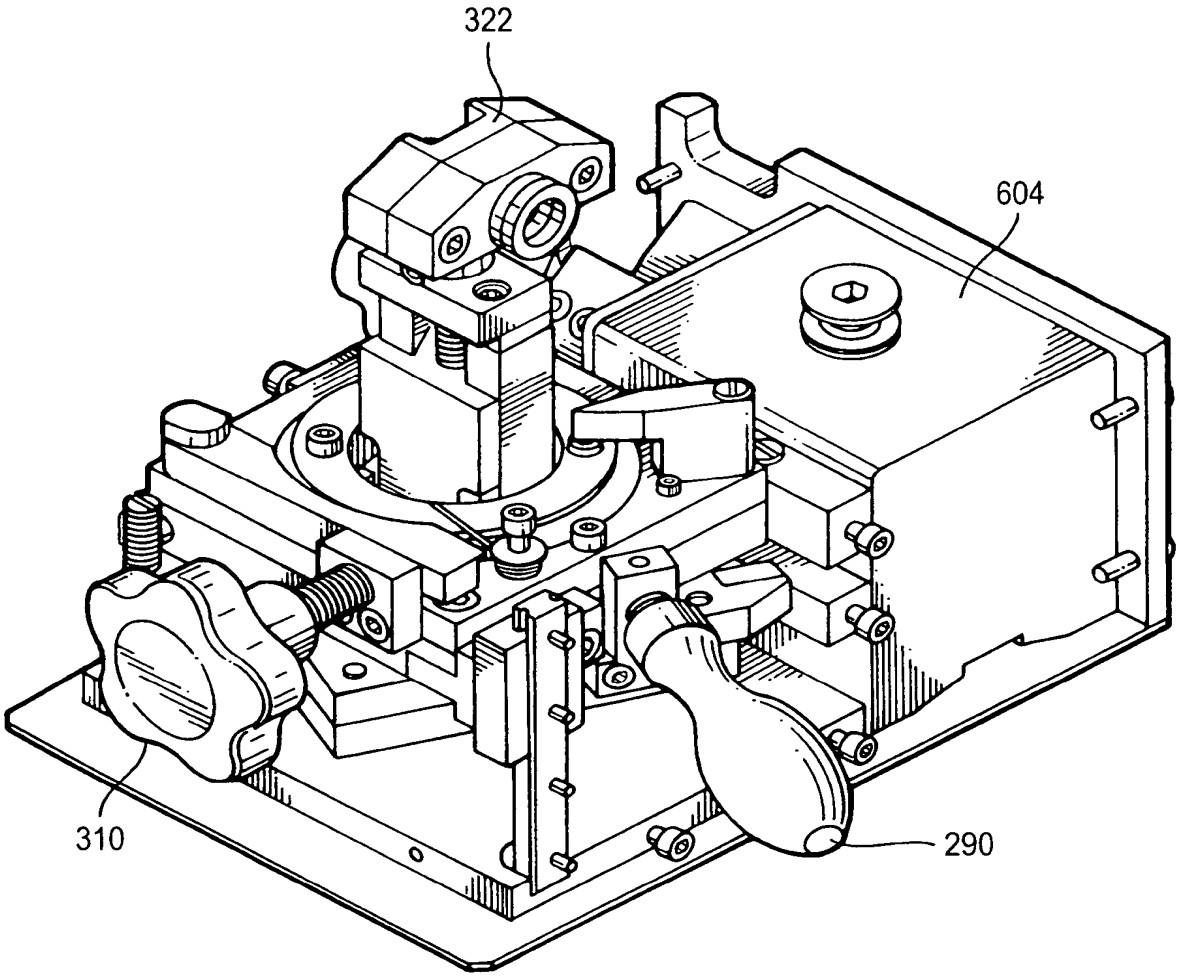

FIG.29

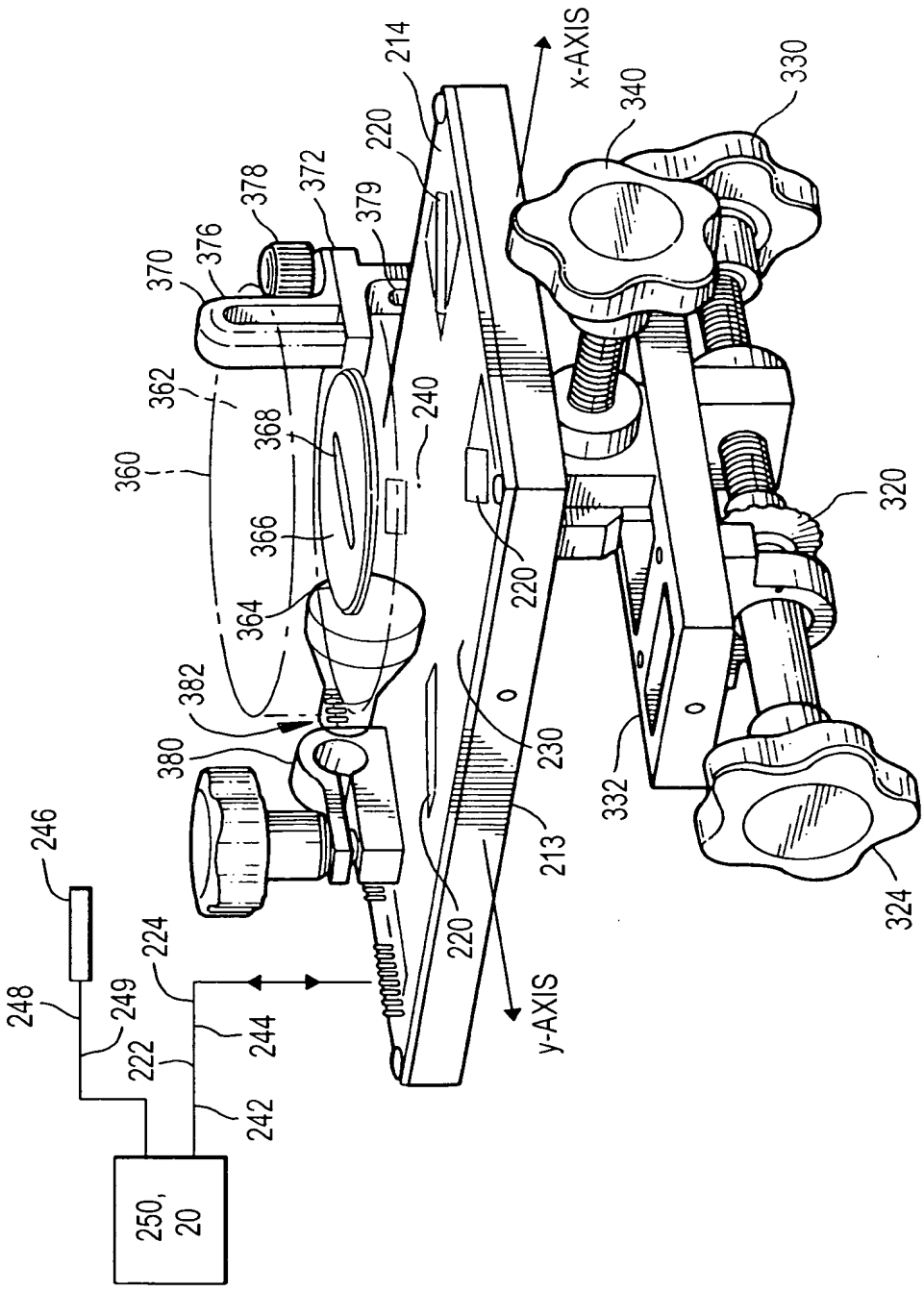
x-AXIS
y-AXIS
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FIG.30

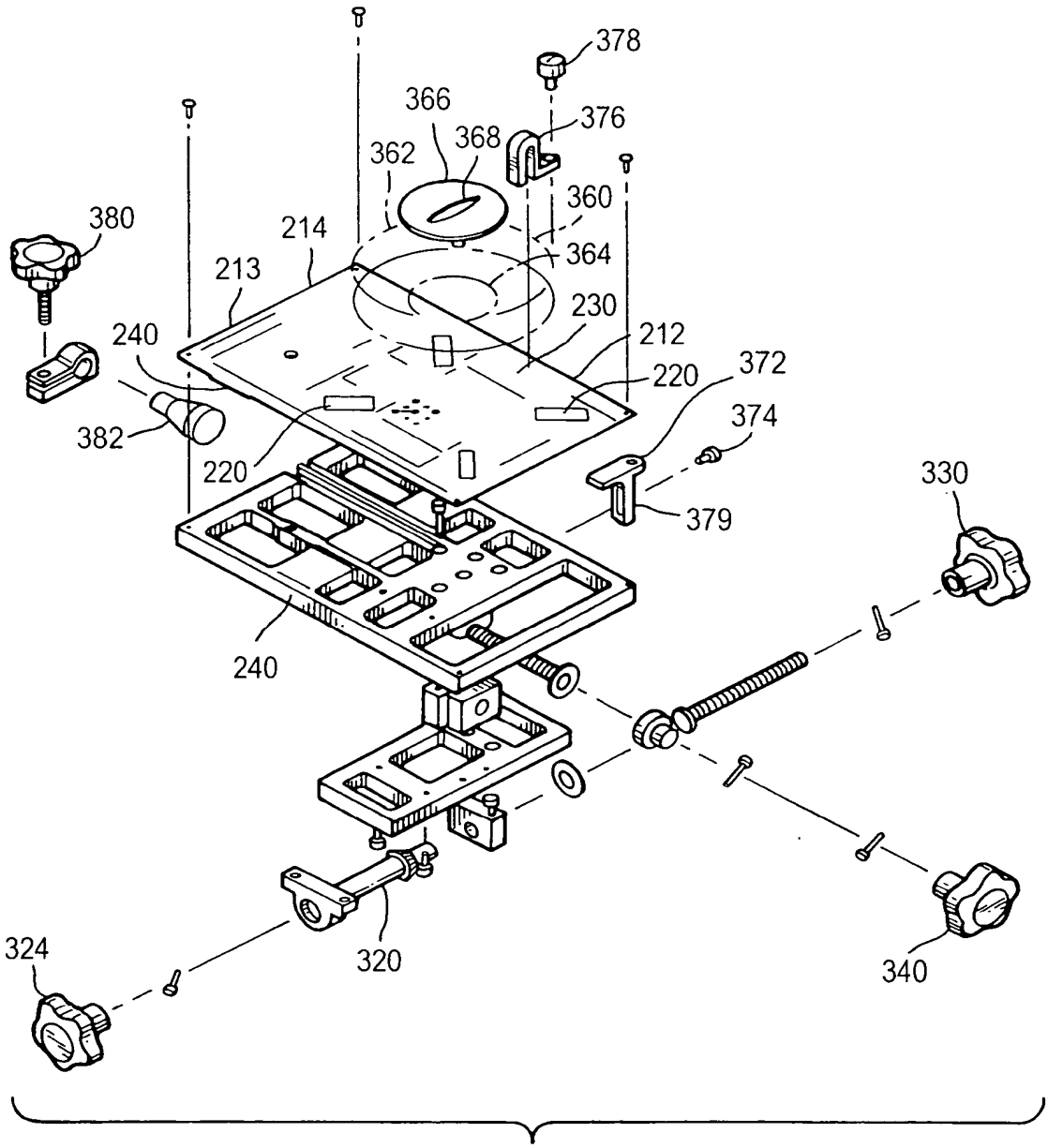
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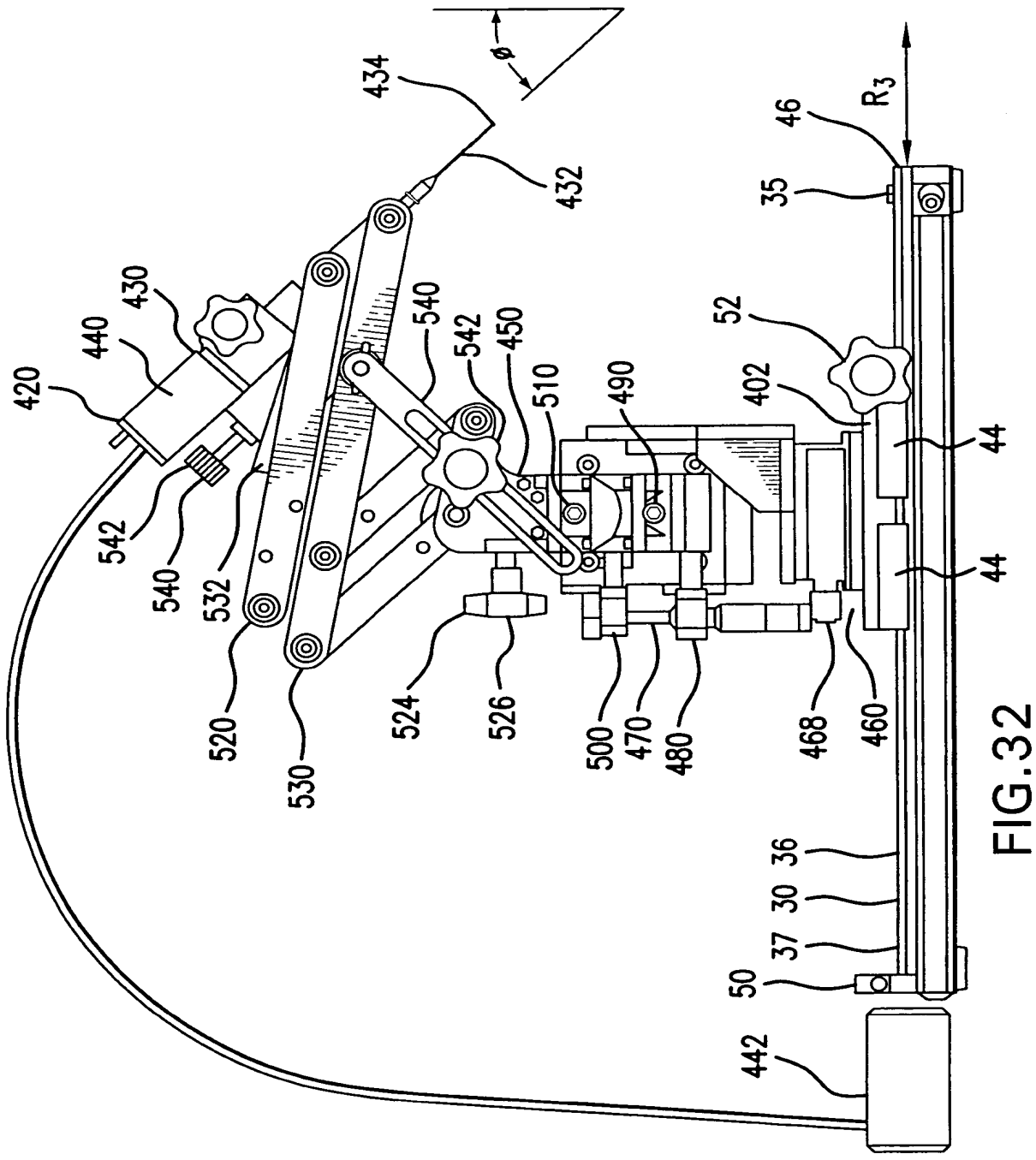
FIG.31


434
ø
432
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R3
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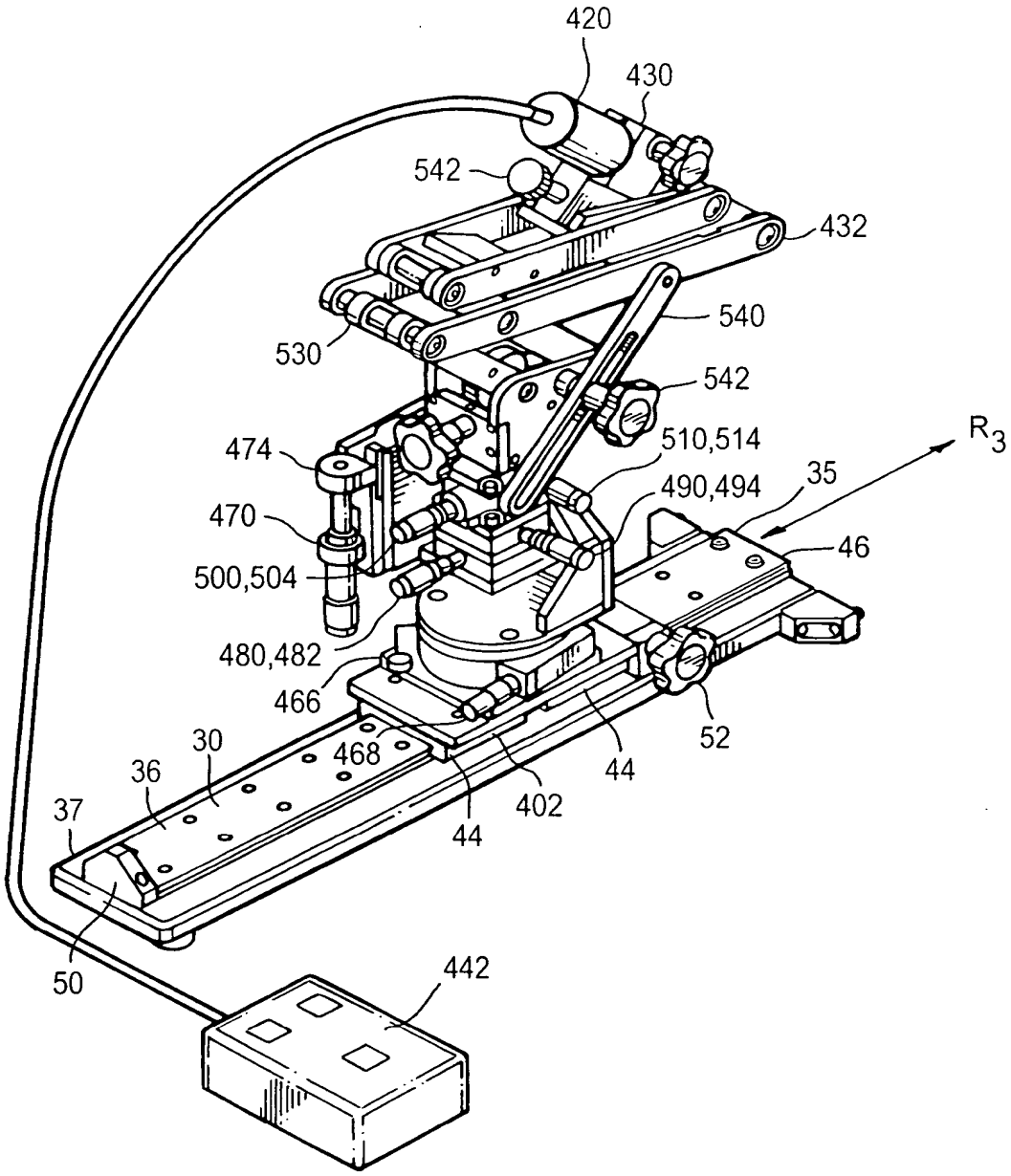
FIG.32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R_3
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FIG.33

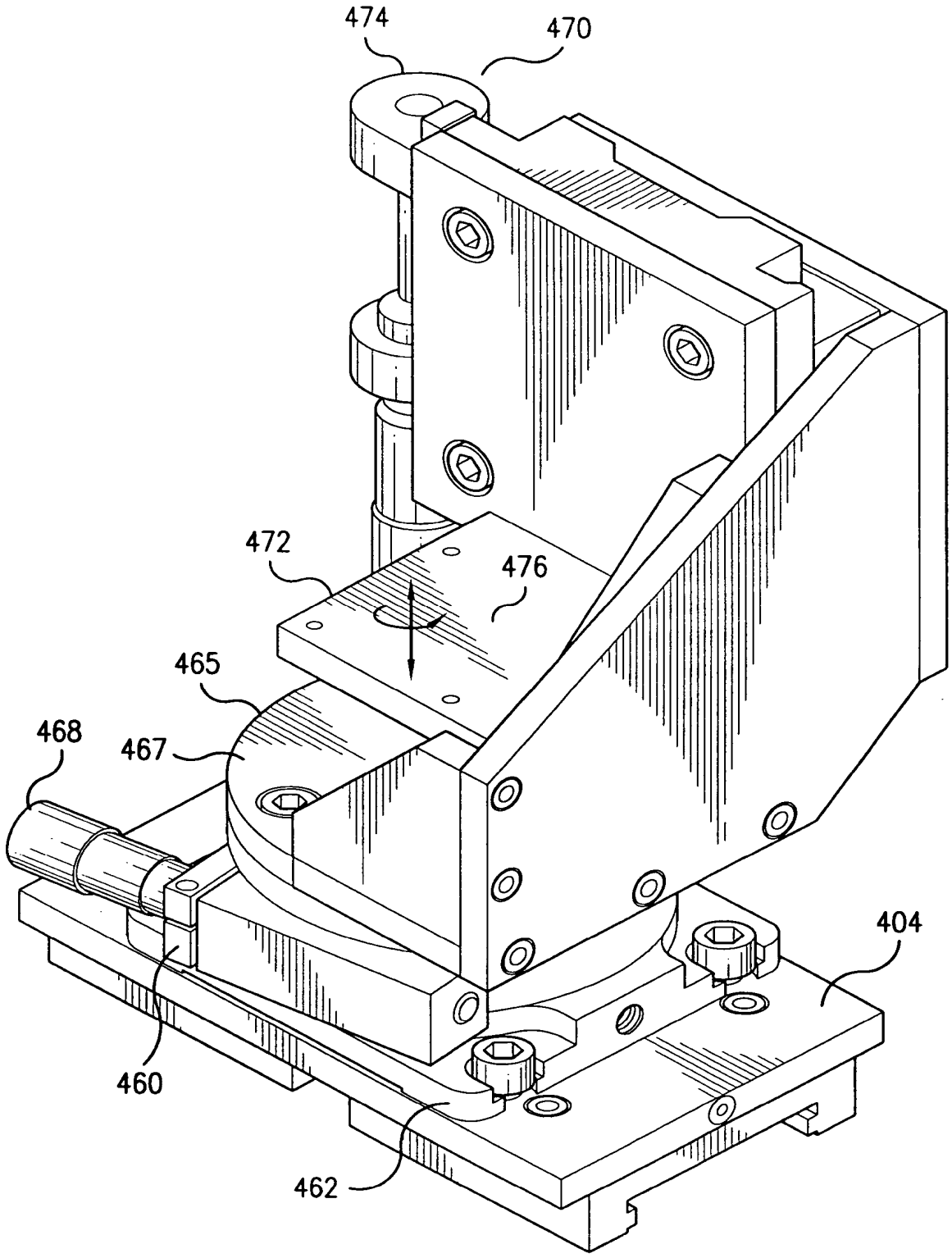

FIG.34

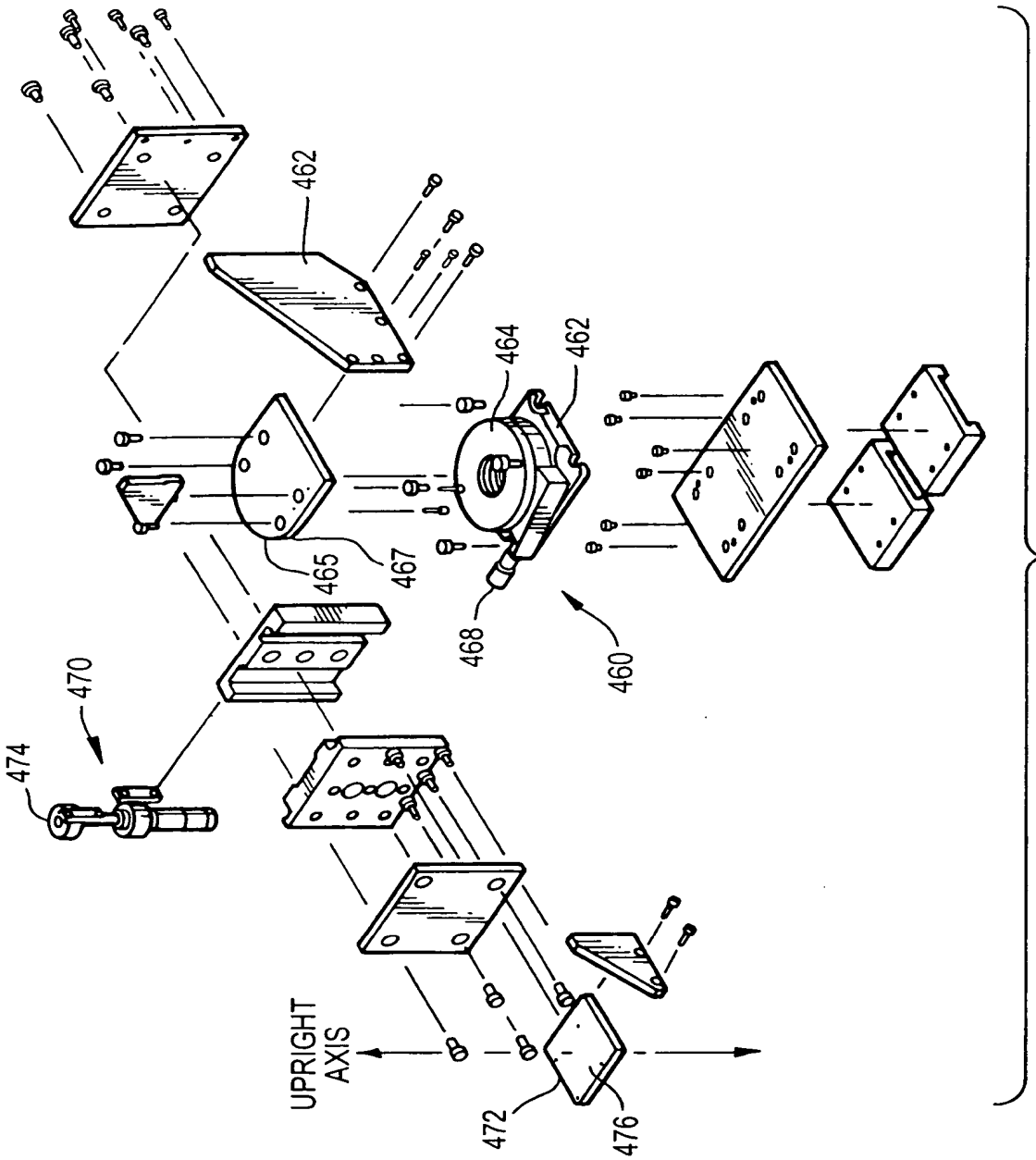

FIG.35

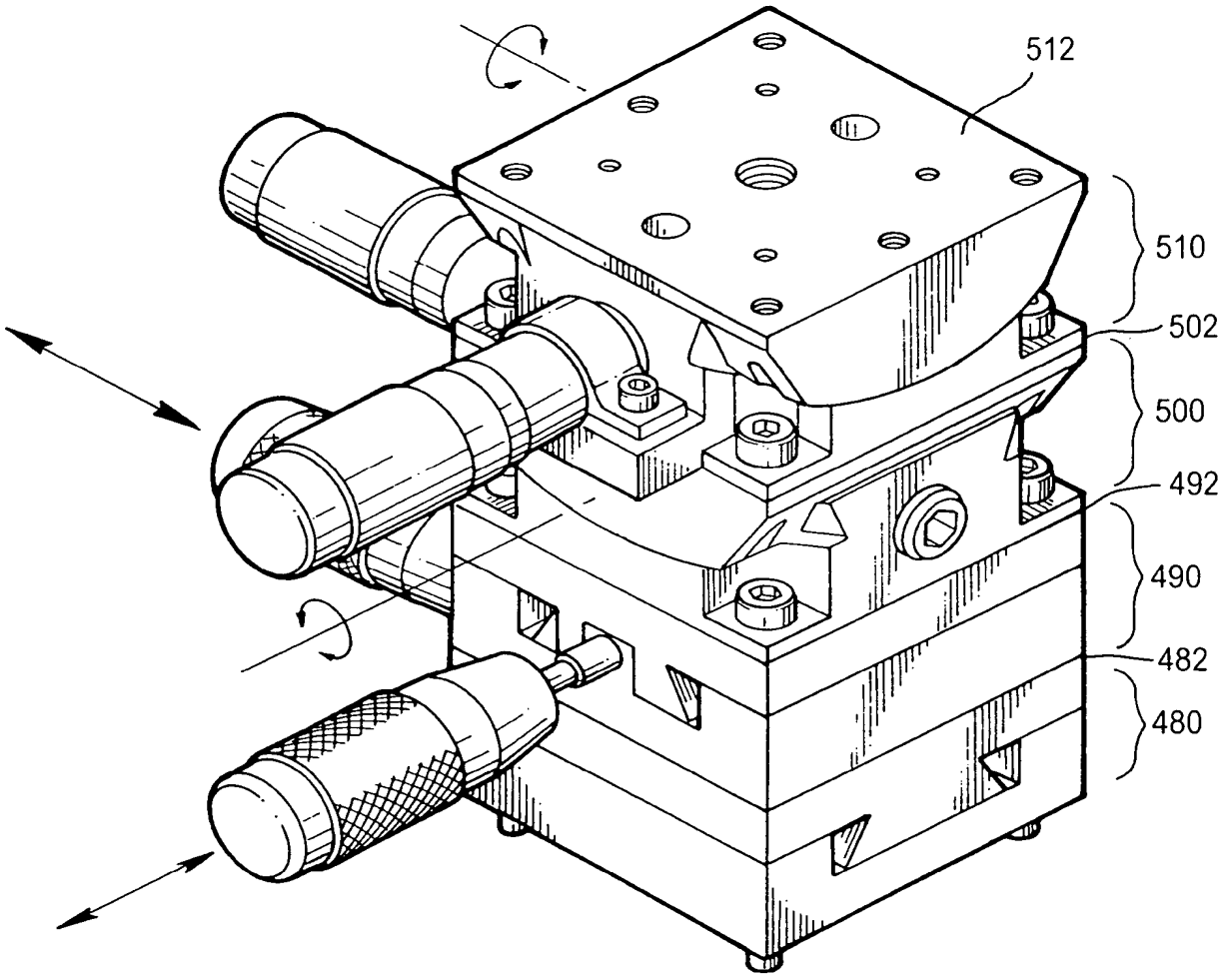

FIG.36

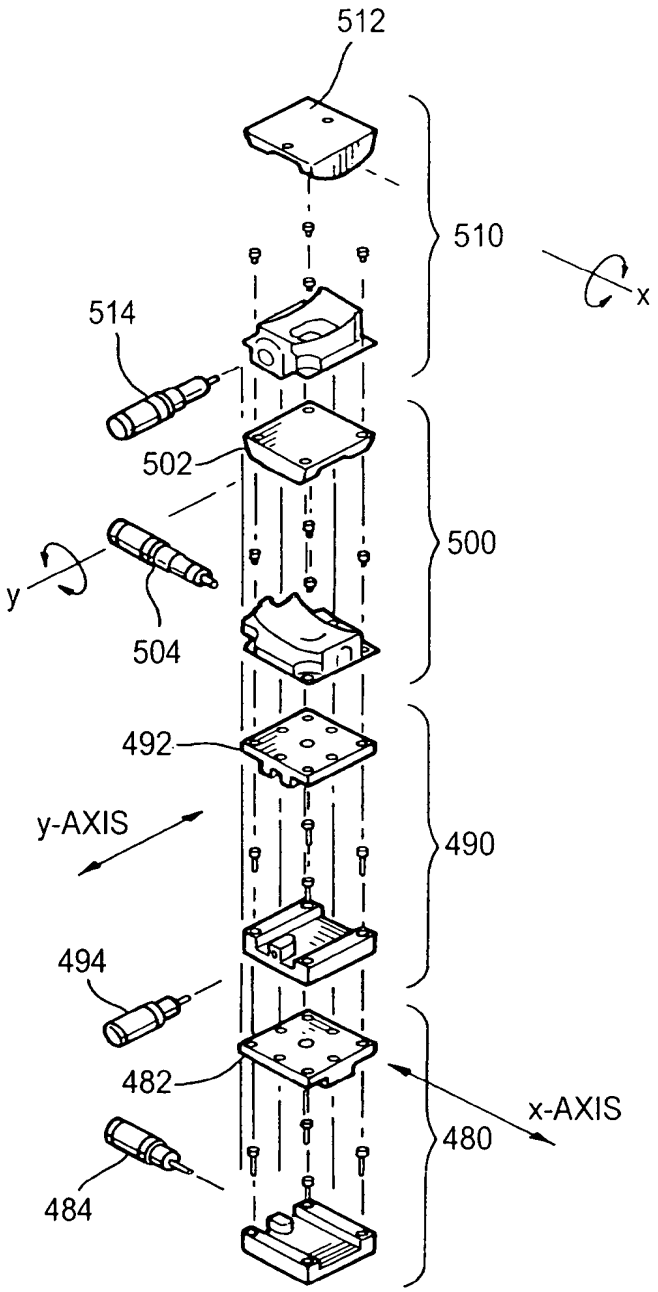

FIG.37

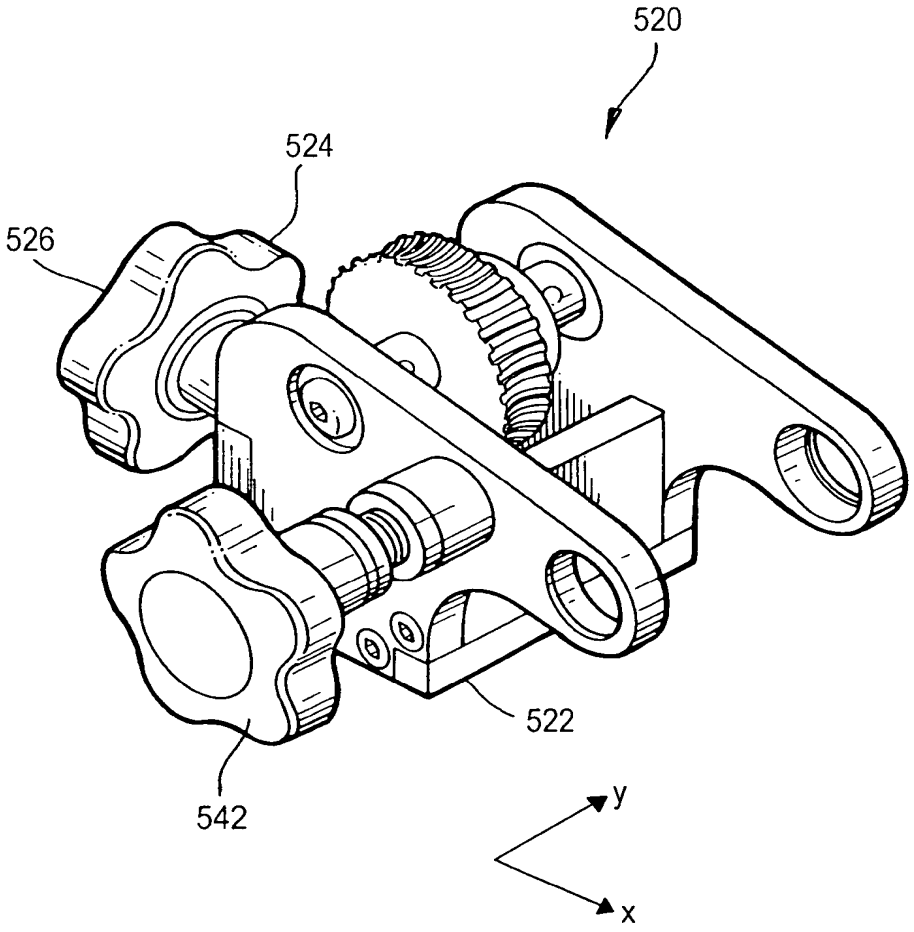

FIG.38

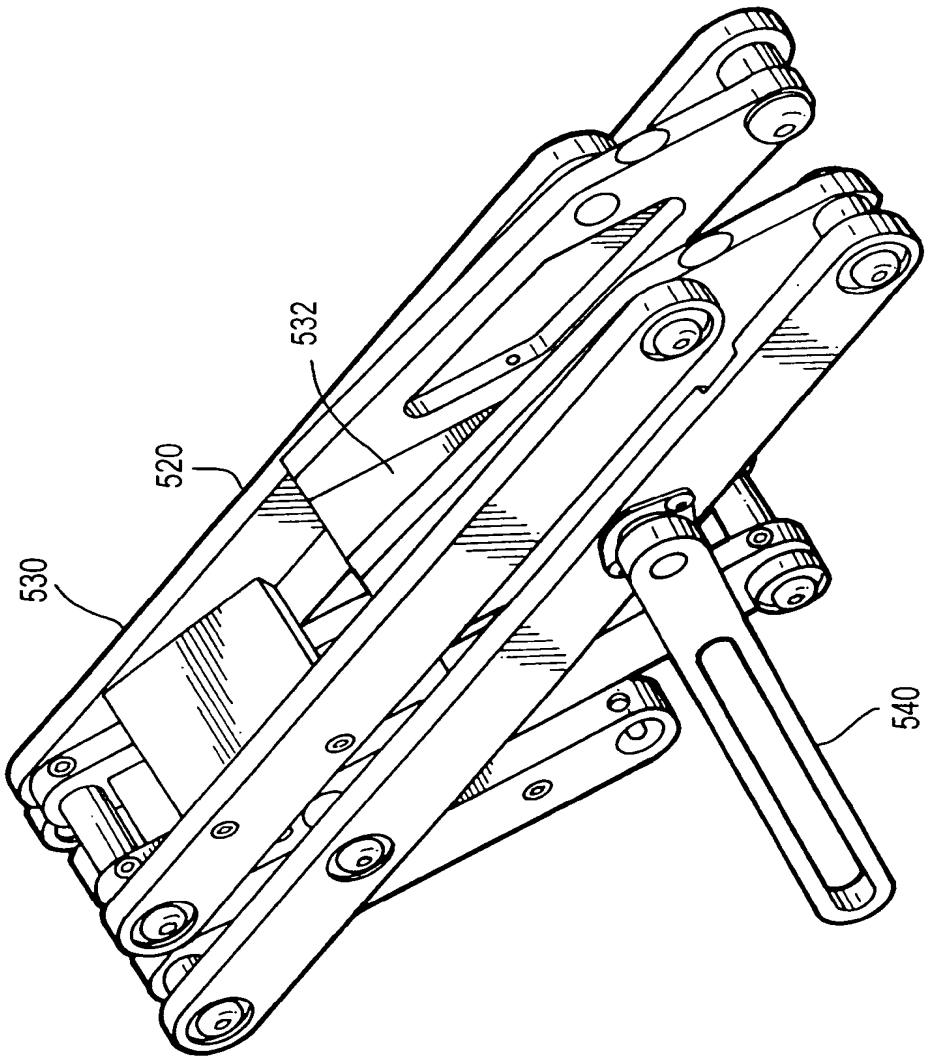

FIG.39

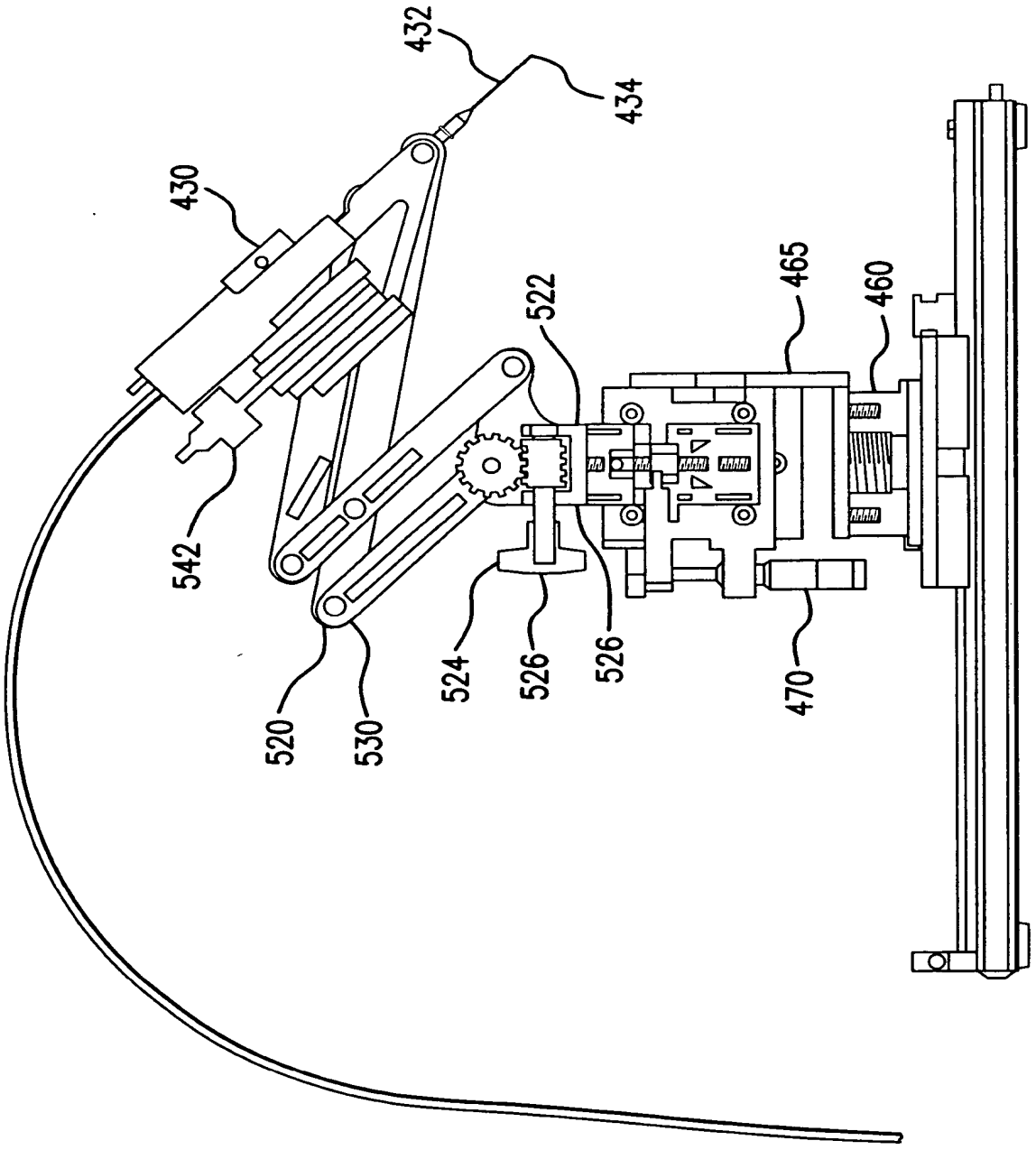

FIG.40

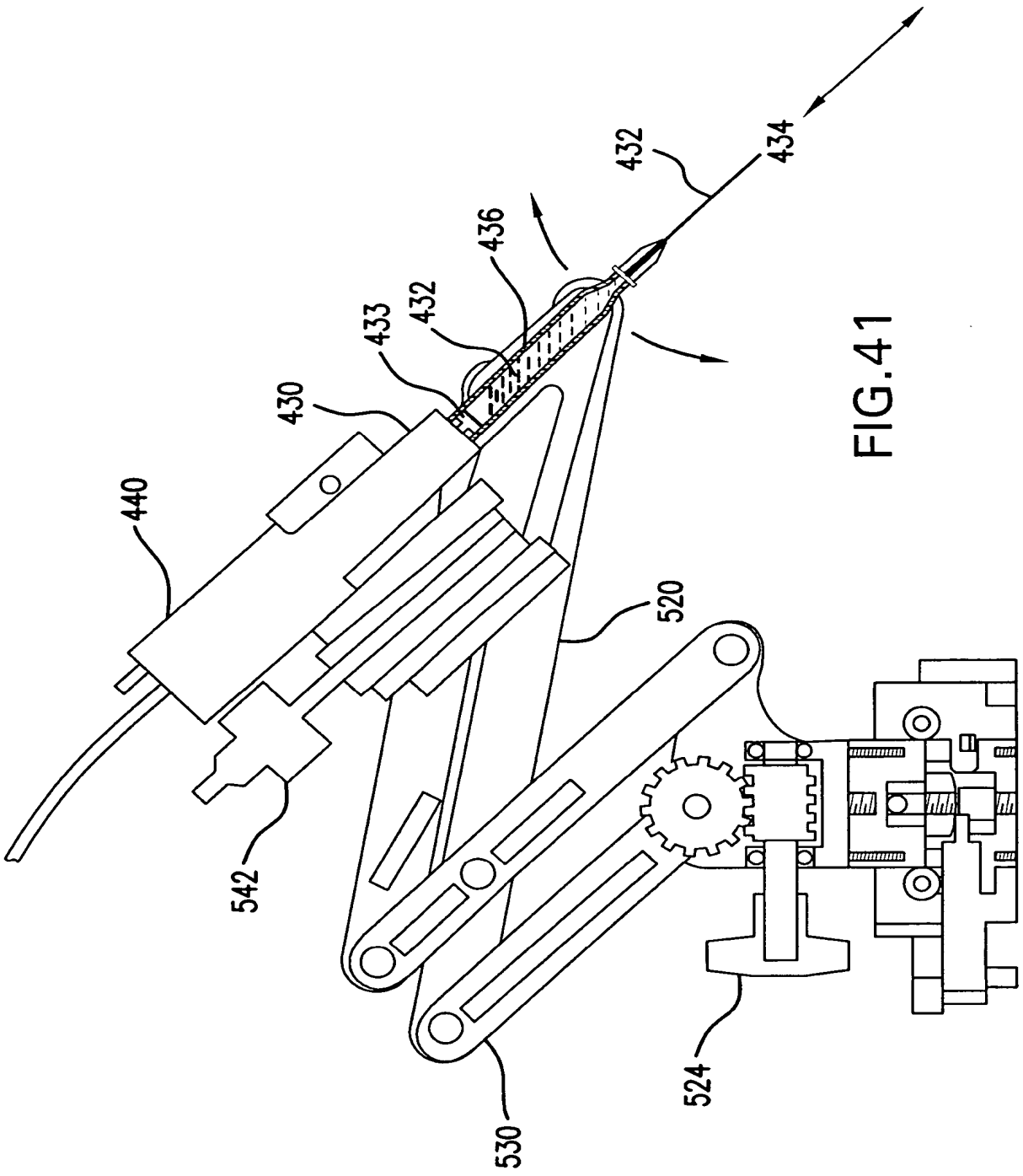

FIG.41

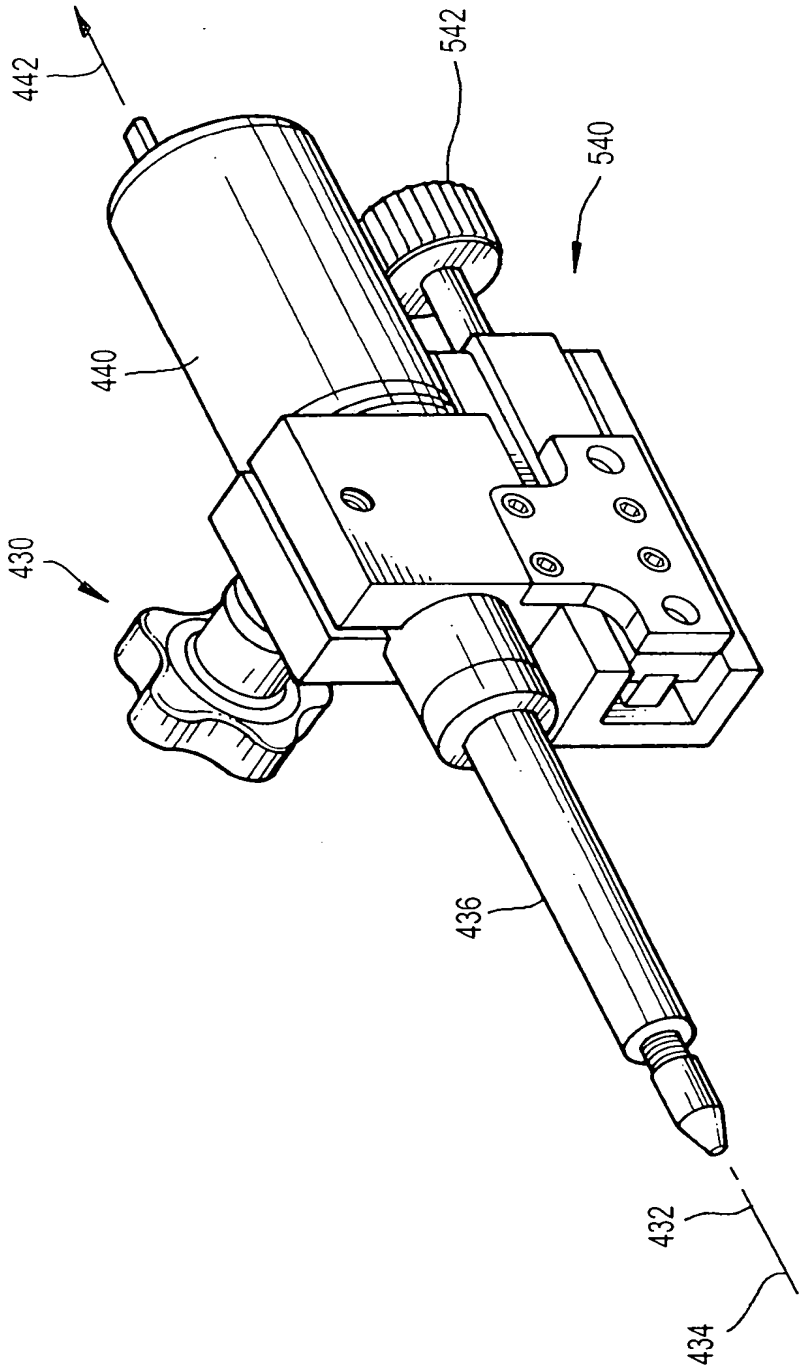

FIG.42

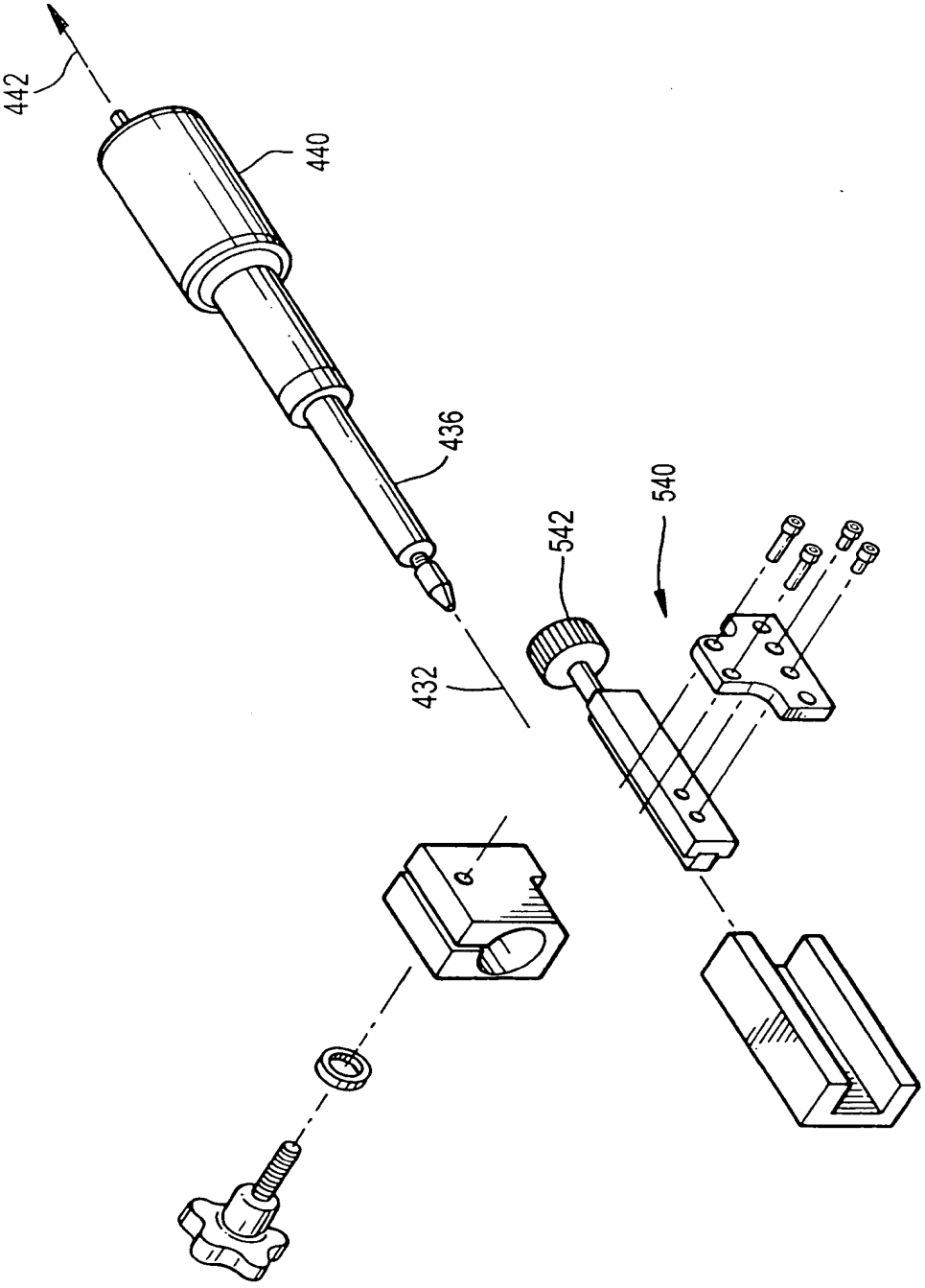

FIG.43